NASA Technical Memorandum 109160



Dynamic Stability Instrumentation System (DSIS) Volume I: Hardware Description

T. L. Jordan, T. S. Daniels, D. A. Hare, R. P. Boyden and D. A. Dress Langley Research Center, Hampton, Virginia (NASA-TM-109160-VOl-1) DYNAMIC STABILITY INSTRUMENTATION SYSTEM (DSIS). VOLUME 1: HARDWARE DESCRIPTION (NASA. Langley Research Center) 91 p

N95-18899

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November 1994

National Aeronautics and Space Administration Langley Research Center Hampton, Virginia 23681-0001

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Abstract

The paper is a hardware description manual for the Dynamic Stability Instrumentation System (DSIS) that is used in specific NASA Langley wind tunnels. The instrumentation system performs either a synchronous demodulation or a Fast Fourier Transform on dynamic balance strain gage signals, and ultimately computes aerodynamic coefficients. The DSIS consists of a double rack of instruments, a remote motor-generator set, two special stings each with motor driven shafts, and specially designed balances. The major components in the instrumentation rack include a personal computer, digital signal processor microcomputers, computer-controlled signal conditioners, function generators, digital multimeters, and an optional Fast Fourier Transform (FFT) Analyzer.

Introduction

This document, the first volume of a three-volume set, describes each of the Dynamic Stability Instrumentation System rack components.

model oscillation technique[1]. These coefficients are functions of the forces and moments on the oscillating wind tunnel model. The DSIS consists of a double rack of instruments, a remote motor-generator set, two special stings The Dynamic Stability Instrumentation System determines aerodynamic stability coefficients using a forced each with motor driven shafts, and specially designed mechanisms which convert the rotary shaft motion into either a pitch, yaw, or roll motion about the balance oscillation center. The major components in the instrumentation rack conditioners, function generators, digital multimeters, and an optional FFT Analyzer. These new racks replace original electro-mechanical resolver-based analog instrumentation[2]. The original motor-generator set used to drive include a personal computer (PC), digital signal processor (DSP) microcomputers, computer-controlled signal the sting drive motor is used in the new system. The second volume is a software manual that describes the PC software at a functional level and includes information on how to modify the C language code. The third volume describes how to operate the system to obtain the desired aerodynamic damping coefficients.

This paper provides a description of the individual components in the DSIS racks. The function of each component is discussed along with its relationship to the rest of the system. Where relevant, electrical schematics have been included for equipment made in-house. Specifications for the programmable signal conditioners and some of the boards within the PC-386 are listed in appendix A. Also included is appendix B, the cable drawings for pertinent cables in the DSIS.

Component Descriptions

Refer to the block diagram of figure 1 labeled "Cable Layout and Block Diagram" and note that each block are connected to the DSIS by cables labelled with "C" and a number. For example, the G-meter Signal is connected to the DSIS rack via cable C62. A recessed panel on the left side of the DSIS rack (front view), called the Junction are processed as they move to the right, or through the DSIS, until they reach the PC-386. A description of each of represents a chassis or other instrument in the DSIS rack. Signals of interest originate from sensors on the left and Panel, is the location of all of the connectors indicated by the diagram with the "J" and number designation. Signals the blocks in the diagram is now given.

PC-386

This industrial-grade, rack-mounted computer is where most of the data acquisition, conditioning, and reduction take place. It is indicated by a dashed block in figure 1. The PC also controls three (of five) separate signal conditioners and a digital multimeter via IEEE-488, an X-Y plotter via RS-232, and a Laserjet Printer via a parallel port. Several boards reside within the PC and are listed in table 1, which lists each board according to its expansion slot number (numbering from left to right, rear view). The computer uses a passive backplane architecture with the actual 80386-based computer residing on a board, plugged into backplane slot number 11. Additional information on the personal computer is given in the Specifications section. The following discussion describes PC-386 components and their cable connections.

DSP-32C

Located within the PC-386 personal computer are two boards that perform the majority of the signal processing. There are two identical DSP boards that are the heart of the entire DSIS, each labelled DSP #1 and DSP #2 on the DSP32C digital signal processor and have specifications listed in appendix A. After digitizing the analog balance drawing in figure 1. These DSP boards plug into PC backplane slots numbered 8 and 9. The boards utilize the WEsignals to 16-bit resolution, the boards implement the signal processing algorithms[1].

DSP#1-2. Cable Cll connects to the top mini-XLR DSP#2-10n DSP board #2 and carries the Secondary analog signal. Cable C9 carries the same Displacement analog signal as Cable C8 and is connected to the bottom mini-XLR Referring to figure 1, cable C8 connects to the top mini-XLR DSP#1-1 on DSP board #1 and carries the Displacement analog signal. Cable C10 carries the Torque analog signal and is connected to the bottom mini-XLR DSP#2-1. All of these cables are connected at the opposite end to the AC CAL Switch Panel.

DAS-20 Board

The DAS-20 is a multifunction I/O expansion board located in PC-386 slot number 14. Specifications for this board are listed in appendix A. An analog angle-of-attack (AoA) signal, originating from the Q-flex accelerometer, is carried by cable C30 from the Main Interface chassis to channel four of the DAS-20 analog input connector. This signal is digitized by the DAS-20 to 12-bit resolution and is used by the computer to calculate and display the model AoA. In addition, cable C30 carries the displacement, torque, and secondary signals to the DAS-20 board on analog input channels one, two, and three, respectively. These signals are currently not being used by the computer, but can be accessed if needed. Up to 12 other analog signals can be acquired and digitized by the DAS-20 via the breakout board within the Main Interface chassis.

Digital signals are also carried by cable C30. Digital input lines DI0 and DI1 are used to read the state of the motor on/off switch and the wind-on switch, respectively. These two signals, along with the calibrate resistor and signal conditioner on/off signals, which exist within the PC-386 software, are used to determine the data code. Two digital signals corresponding to the state of the calibrate resistor and signal conditioner on/off software switches are sent out on DO0 and DO1 on cable C30, through the Main Interface chassis and on to the digital port of the motor speed controller (where the data code display resides). Three additional channels of digital input or output are available for use via the breakout board.

IEEE Interface Roard

This board plugs into PC-386 slot number 13 and enables the PC-386 to interface to any IEEE-488 compatible instruments. The board enables access to all internal settings of each of the three programmable signal conditioners via cable C1. Also, the board is used during calibration to configure and acquire data from the Keithley 2001 digital multi-meter (DMM) via cable C38. The IEEE addresses of the programmable signal conditioners and DMM are 10 and 16, respectively. The HP 3325A and HP3245A Function Generators can be configured via IEEE to automate the calibration process. Refer to DSIS Volume III User Manual for descriptions and procedures of the various calibrations required.

Shaft Encoder Simulator Board (SESB)

labeled "SESB." The schematic for this board is shown in figure 2. Notice in figure 2, shaft encoder pulses from the This is a custom-made circuit board that resides in PC-386 slot number 5. It is indicated in figure 1 by a box RPM Indicator chassis are routed to two sets of digital switches (Quad Switches U6 and U7) on this board through connector J3. Similar signals are generated on board (U1 through U5 and U8) and are also routed to the switches, providing either real or simulated encoder signals. These switches then route the selected signals to connector J2 and at a frequency of $8192 \times \omega$ to generate these simulated encoder signals, where ω is the oscillation frequency of the on to the Tachometer board. A TTL square wave from the HP 3325A function generator is used to drive the SESB pitch-yaw balance. Control of the digital switches is provided by a single manual switch labeled "Cal Enable" which is located on the front panel of the RPM Indicator chassis. Thus, the user must select the type of encoder signal to match the mode of operation: simulated signals for calibration purposes or real signals for normal operation.

Tachometer Board

boards via a 32-bit data bus. A schematic for this board is shown in figure 3. The quadrature shaft encoder signals This circuit board resides in PC-386 slot number 6, counts encoder pulses, and interfaces directly to the DSP (or simulated signals) from the SESB via cable C36 are combined by a logical EXCLUSIVE OR operation resulting in 8192 clock pulses per revolution. An onboard 1 MHz oscillator is counted to determine the oscillation frequncy $8192 \times \omega$. The 1/REV index pulse from the encoder via the SESB is used to reset the counters. Timing pulses are then sent from the board to the RPM Indicator chassis through cable C14 to drive the digital RPM display.

Printer (HP Laserjet II)

An HP Laserjet II printer is connected to the parallel port of the PC-386 via cable C41. The Laserjet is used to produce hard copies of the raw data (voltages) and the data after it has been reduced (coefficients).

X-Y Plotter

An X-Y plotter is connected to the serial port COM1 of the PC-386 via cable C34. This plotter is used to produce plots of the selected coefficients.

VGA Monitor

A color VGA monitor is connected to the PC-386 Graphics Card via cable C58.

Junction Panel

assignment and connector type is listed in table 2. Cables from the instrument rack are hard wired into the back of The Junction Panel is a recessed panel on the left side of the DSIS rack. This panel serves as the junction for signals coming from the tunnel and the DSIS. Connectors are numbered and labeled J1 through J12. Each connector the connectors. Cables from the tunnel are terminated in mating connectors. Once the DSIS rack is located within the "data room," cables can be connected to the DSIS.

The G-meter signal on cable C62 connects to the Junction Panel at J9 and the output is fed through cable C15 to the signal access panel for viewing on the oscilloscope. Power for the G-meter is provided by the heater controller and is routed to the junction panel through cable C16. A small metal enclosure located within the rack near the junction panel contains the signal conditioning for the G-meter. A schematic of the signal conditioning circuit is shown in figure 4.

The signal is then fed out the back of the panel on cable C18 to the programmable signal conditioners. The torque and secondary signals are routed similarly to the displacement signal: through cable C64, connector J2, and cable The displacement signal from the balance is routed to the junction panel thru cable C63 and is connected at J3. C19 for torque; and through cable C65, connector J1, and cable C20 for the secondary signal.

Power for the balance heaters is carried by cable C66 from connector J5 of the Junction Panel. The temperature sensor leads from the balance are routed to connector J4 thru cable C31. The Heater Controller routes power and temperature sensor leads for the front heater/sensor pair together on cable C21 and for the rear heater/sensor pair on cable C22 to the back of the Junction Panel. At the panel, power and sensor leads are split apart. The power for both heaters is routed to connector J5 and the temperature sensor leads are routed to J4.

Model attitude is measured with a Q-FLEX accelerometer whose signal is carried via cable C67 to Junction Panel connector J7 and out through cable C23 to the Q-FLEX signal conditioning chassis. Sting attitude is set by a motor attached to the sting and is powered by cable C59. This cable is attached to the Junction Panel at J10 which feeds through to the Motor Speed Controller Chassis via cable C25. Limit switches for this motor are wired through cable C60 to J11 at the Junction Panel and feed through to the Motor Speed Controller along cable C26.

at Junction Panel J6. These signals originate from the Motor Speed Controller Chassis and are routed to the back of The frequency of oscillation of the model is measured by an encoder coupled to the sting motor. Signals from the encoder travel via cable C29 to Junction Panel connector J8 and out through cable C24 to the RPM Indicator Chassis. The sting motor is powered by a motor-generator set which is controlled by signals on cable C68 connected Junction Panel connector J6 on cable C27.

A switch mounted to the side of the sting detects wind-off and wind-on conditions. Cable C61 connects this switch closure to the Junction Panel at J12. The signal continues along cable C32 to the Data Code Display Panel.

Function Generator (HP3325A)

This instrument is a function generator that is used to simulate a shaft encoder. It provides a TTL square wave of the pitch-yaw balance. This signal originates from the SYNC OUT connector, travels via cable C37 to J1 of the signal to drive the shaft encoder simulator board at a frequency of $8192 \times \omega$, where ω is the oscillation frequency SESB. The shaft encoder signal (or the simulated signal) frequency is divided by 8192 on the tachometer board within the PC and results in the frequency, ω, at which the balance signals are demodulated. The function generator can be set to 0 Hz (square wave signal) and the RPM Indicator front panel encoder switch set to the position labelled "Cal Enable" in order null the DC offset from the programmable signal conditioners. The same signal on C37 also travels on C47 to the Signal Access Panel for viewing on the oscilloscope. Also, cable C46 is connected at the REFERENCE OUT connector and carries the frequency reference to the HP3245A, FREQUENCY REFERENCE connector, to keep the two function generators synchronized.

Function Generator (HP3245A)

This function generator is used for internal calibration of the rack equipment. It is not used during aerodynamic testing. Through front panel input, the instrument generates low frequency sine waves of specified frequency and phase to be used as calibration signals. These signals can be switched into the programmable signal conditioners in place of the displacement on cable C43, secondary on cable C45, and torque signals on cable C44. They are synchronized to the output of the HP3325A (shaft encoder simulator). In addition, a simulated shaft encoder once Chassis. During AC Calibration, a sinewave signal generated at CHANNEL B OUT by this function generator is carried to the AC CAL Switch Panel along cable C51 to connector HPB. per revolution (1/REV) signal arrives at connector TB1 via cable C39 from connector J4 of the RPM Indicator

Programmable Signal Conditioners

with the host computer over the IEEE-488 bus on cable C1. Each signal conditioner is configured with the specifications listed in appendix A. The rack can hold up to eight separate signal conditioners, but only three are used mounted chassis. This chassis also includes a microcontroller that communicates with each signal conditioner and The programmable signal conditioners are separate instrumentation amplifiers and filters mounted within a rack

of the signal conditioner chassis. The torque balance signal via Cable C19 (or the calibration signal via Cable C44) arrives at J1. Also, the secondary balance signal via Cable C20 (or the calibration signal via Cable C45) arrives at The displacement balance signal via Cable C18 (or the calibration signal via Cable C43) arrives at J0 at the rear

signal emerges from connector J11, splits and travels on cable C3 to connector IB2 of the Main Interface Chassis The conditioned displacement signal emerges from connector J10, splits and travels on cable C2 to IB1 of the Main Interface Chassis and cable C52 to the AC Cal Switch Panel, connector DISP1-IN. The conditioned torque and cable C54 to the AC Cal Switch Panel, connector TORQ-IN. The conditioned secondary signal emerges from connector J12, splits and travels on cable C4 to connector IB3 of the Main Interface Chassis and cable C55 to the AC Cal Switch Panel, connector SEC-IN.

Each signal conditioner is equipped with a bridge completion card (BCC) that is front panel accessible. Located on each BCC are the calibration shunt resistor pairs and several jumpers to configure the BCC. Each BCC is configured with a set of shunt resistor pairs as shown in table 3. The second column in this table lists the value of each of the shunt resistors while the third column lists the corresponding signal amplitude (Channel Gain = 1) computed using a bridge resistance of 350 Ohms and a bridge voltage of 5.0 Volts. These values will change if the gain is increased since the shunt resistors are connected across the balance arms prior to the gain stage within the signal conditioners.

Heater Controller

The balance incorporates two temperature sensors and two heaters. The Heater Controller has two independent heater is controlled. An operating temperature (150°F) is set on the front panel of the temperature controller along with upper and lower limits (±4°F about the set point). The controllers are set up to use a three-wire RTD input. This channels mounted together in one chassis. Each channel consists of a Kepco power supply and a Shimaden temperature controller. The front panel of each controller is labelled "Front" or "Rear," indicating which balance minimizes errors in the temperature reading due to lead wire lengths. The heater and temperature sensor leads for the front channel are routed through cable C21, which is connected to the rear of the Heater Controller chassis. Cable Figure 5 is a schematic of the Heater Controller chassis, which is used to control the temperature of the balance. C22 has similar leads for the rear channel. Once these cables reach the junction panel, the heater leads are paired together into connector J5 and the temperature sensor leads are paired together into connector J4.

Q-Flex Signal Conditioner

This two-channel instrument is used to process the angle-of-attack (AoA) accelerometer current signal to a The instrument also provides power for the accelerometer and low-pass filtering for the output. The output voltage signal is sent to the DAS-20 board through the Main Interface Chassis. This output signal can be calibrated to proportional voltage signal by providing a precision load resistor. Figure 6 is a schematic of the signal conditioner. indicate AoA given the proper constants. Only one channel of the instrument is used. The accelerometer output on cable C23 arrives at connector Q-FLEX Input at the rear of the chassis. The conditioned signal is routed through the FIL OUTPUT connector through cable C12 to connector IB4 of the Main Interface Chassis.

G-meter Output Circuit

This circuit is located within a small grey metal enclosure mounted just below the Junction Panel and its schematic is shown in figure 4. Its purpose is to provide signal conditioning for a piezoelectric accelerometer mounted in the sting. A five-volt dc power supply in the heater controller chassis provides the excitation for this circuit via cable C16. The output of this circuit is routed on cable C15 to the Signal Access Panel. The signal can be monitored visually using the fourth channel of the oscilloscope to insure that sting vibrations do not exceed specified limits. (The first three channels are used to monitor the three balance signals.)

RPM Indicator

This chassis provides one of two independent measurements of the shaft encoder angular velocity. Figure 7 is Pulses from the encoder enter the chassis via cable C24 through connector J2. These pulses are routed directly to connector J3, and together with a calibration enable signal they are sent via cable C17 to connector J3 of the Shaft Encoder Simulator Board. The calibration enable signal depends on the state of a switch labeled "Calibrate Enable schematic of this chassis. A four-digit LED display on the front panel indicates this measurement in units of Hertz. / Encoder" located on the front of the chassis. Also on connector J3 is a 1/REV pulse coming back from the Shaft Encoder Simulator Board. This signal is passed out of the RPM Indicator chassis through connector J4 on cable C39 and on to the HP 3245A function generator where it is used as a synchronizing pulse. Timing pulses are received by connector J1 over cable C14 from connector J3 of the Tachometer board in the PC. These are the pulses that are used to drive the display on the front panel.

Motor Speed Control and Data Code Display

frequency, and display of the data code. A schematic of this chassis is shown in figure 8. The model attitude is This chassis provides control of the sting mechanism for model attitude adjustment, control of the oscillation manually adjusted using an up/down toggle switch located on the front panel of the chassis. Large amplitude current supplied by the AoA Drive 40 VDC power supply along cable C28 enters the chassis through connector MC2 and passes through the up/down switch. This current is then routed toward the AoA motor on cable C25 from connector MC4. The switching of this high current causes electrical noise on the data lines in the rack; however, since data is not taken while model attitude is being changed, this is not a problem. Two limit switches, which are located on the sting, are connected to the chassis via cable C26 through connector MC3. Control of the oscillation frequency of the model is achieved by a potentiometer labelled "Frequency" on the front panel of the chassis. This potentiometer sets a control voltage through a divider circuit. This control voltage is then sent out through rear connector MC1 through cable C27 to the Junction Panel connector J6 and on to the MG set, which drives sting oscillation mechanism.

The data code is used to associate certain parameters with a set of data. Data codes are determined by the states of two hardware and two software switches. The two hardware switches are the motor on/off switch, located on the front panel of the Motor Speed Control chassis and the wind-on switch, located on the sting. The state of the wind-on switch is transmitted to the chassis via cable C32 through connector MC6. The software switches correspond to the state of the programmable signal conditioners and the calibration resistors. The states of the software switches are transmitted to the Data Code Display through the digital output port from the DAS-20 board. Connector MCS transmits the state of the hardware switches to and receives the state of the software switches from the DAS-20 board. The values and corresponding data types for the data codes are listed in table 4.

AC Cal / Normal Switch Panel

A schematic of this panel is shown in figure 9. This panel provides a manual switch that connects all DSP inputs to one output of the HP3245A function generator (AC CAL switch selection), or to the appropriate programmable signal conditioner (NORMAL switch selection). "AC CAL" means AC calibration mode while "NORMAL" means normal operating mode. The OPEN switch selection disconnects all inputs from all outputs and is not used. During NORMAL switch operation, the conditioned displacement signal, which is on cable C52, connects to the DISP-1 input and is routed to the DISP-1 output connector where cable C8 carries it to DSP#1-1 in the PC-386. Cable C9 carries it to DSP#2-2, and cable C5 carries the signal to the Signal Access Panel. This same conditioned displacement signal is fed through the Main Interface Chassis and emerges from connector IB10, traveling along cable C53 to connector DISP-2. The conditioned torque signal, which is on cable C54, connects to the TORO input and is routed to the TORQ output connector where cable C10 carries it to DSP#1-2 in the PC-386 and cable C6 carries it to the Signal Access Panel. The conditioned secondary signal, which is on cable C55, connects to the SEC is carried via cable C56 to the KEITHLEY input connector. This signal is internally routed to the KEITHLEY output connector where it then travels to the rear input connector of the Keithley 2001 DMM via cable C57. input and is routed to the SEC output connector where cable C11 carries it to DSP#2-1 in the PC-386; and cable C7 carries it to the Signal Access Panel. One of the three conditioned balance signals from the Keithley Switch Panel

During AC CAL switch operation, sinewave signals generated by the HP3245A function generator are carried via cable C51 to connector HPB-IN. Internal switches route this signal to the following output connectors: DISP-1, DISP-2, TORQ, SEC, KEITHLEY. In addition, the HPA and HPB output connectors are unterminated open circuits.

Main Interface Chassis

balance and (model attittude) accelerometer signals. Connectors provide the user with access to monitor these signals A schematic of this component is illustrated in figure 10. This chassis provides a means of accessing conditioned using an oscilloscope. The chassis also houses the break-out board for the DAS-20 board for digital and analog I/O. A description of the chassis follows. The displacement signal from the programmable signal conditioner on cable C2 enters the chassis through rear panel BNC connector IB1. This signal is redirected through the chassis to the rear panel BNC connectors IB10 and IB11. The displacement signal is used by both DSP boards. The coaxial shield is the signal common. In addition, the displacement signal can be accessed for display on an oscilloscope by using rear panel BNC connector IB12.

The torque signal from the programmable signal conditioner on cable C3 enters the chassis on connector IB2. This signal is redirected through the chassis to connector IB6. In addition, the torque signal can be accessed for display on an oscilloscope by using connector IB7.

This signal is redirected through the chassis to connector IB8. In addition, this signal can be accessed for display by The secondary signal from the programmable signal conditioner on cable C4 enters the chassis on connector IB3. using connector IB9. In order to provide for future capabilities, the displacement, torque, and secondary signals are also connected to a separate analog-to-digital conversion (ADC) board in the PC (the DAS-20 board). This connection is accomplished by screw terminal block TB-1 inside the screw terminal labelled STA-20, as shown in drawing number 6461. A internal 50-conductor flat ribbon connects the screw terminal to the chassis rear panel at connector IB14. Cable C30, a 50-conductor flat ribbon cable, connects to the DAS-20 board inside the PC-386.

The model attitude signal on cable C12 from the Q-Flex instrument is connected to IB4, a male KPT type connector. Notice in figure 10 that the operational amplifier (op-amp) U1 acts as a buffer for the attitude signal. Opamp U2 amplifies the buffered signal. In addition, op-amp U3 adds a variable DC voltage offset to the attitude signal. Within the Main Interface Chassis, the model attitude signal is routed to channel four of the DAS-20 analog input. The user can set this offset level by adjusting front panel access potentiometer R6 labelled "AoA Offset".

Test points are provided to monitor the attitude signal at various stages and for hardware checkouts. An oscilloscope channel can be connected to the front panel BNC labelled "TEST POINTS." A switch is provided to monitor the following signals: raw output of the Q-flex instrument on position "1," output of the buffered stage on position "2," output of the fixed gain of 40 stage on position "3," and output of the DC offset stage on position "4." Other test points provide checks on +15 VDC and -15VDC.

A data code is passed through the main interface chassis at rear panel connector IB15 along cable C33. The on/off and wind-on signals are inputs to the computer while the calibrate resistor and amp on/off signals are outputs conductors in this cable pass digital information to and from the computer via the DAS-20 interface board. The motor to the Motor Speed Controller Chassis.

Oscilloscope (Tektronix 5111A)

A four-channel analog oscilloscope is used to visually monitor the three signals from the output of the AC Cal/ Normal Switch Panel. These may be either the displacement, torque, and secondary signals from the balance (after the programmable signal conditioners), or the AC calibration signals from the HP 3245A. The scope displays the same signals that the DSP boards receive, which depend on the AC Cal/Normal switch. A fourth signal, the G-meter output, is also monitored here. Other signals that can be monitored include the TEST POINTS from the Main Interface Chassis, the sinewave and encoder signals from the two function generators, and the calibration synchronization (TTL level) signal from the HP3325A.

Signal Access Panel

The Signal Access Panel is simply a focal point for accessing several different signals. The three balance signals or AC calibration signals (depending on the AC Cal/Normal switch) are routed to this panel to be displayed on the scope. The G-meter signal is routed here before going to the scope. The SYNC OUT pulse from the HP3325A is also routed here for monitoring. The incoming signals are fed through the back of the panel to BNC connectors located

Digital Multimeter (Fluke 75)

BNC labelled "Displacement" using cable C35. This cable is long enough to extend from the DSIS back to the test This hand-held DMM is used to measure the displacement signal voltage accessed via the Signal Access Panel section, where the user can manually adjust model attitude. A hand crank is inserted into the bottom rear of the sting and rotated until the DMM indicates the "null" value. Turning the hand crank causes the model to change attitude. This is done to position the model correctly for balance calibration.

FFT Analyzer (Ono Sokki CF 6400)

A four-channel, 16-bit FFT analyzer was incorporated into the DSIS to provide a alternate method of computing the dynamic stability coefficients. Instead of the conditioned balance signals going to the DSP boards for processing, they are routed to the FFT analyzer. The analyzer then computes frequency, magnitude, and phase information for the balance signals. Once these computations have been made, the information is stored to floppy disk by the operator and used in a data reduction program. From here, the dynamic stability coefficients can be calculated [3]. The analyzer is not required for normal DSIS operation and can be removed at any time. Digital Multimeter and Keithley Switch Panel (Keithley 2001)

The instrument is a digital multimeter used to measure the conditioned balance signals during calibration and to monitor these signals during normal operation. This meter is controlled by the PC-386 via the IEEE-488 cable C38. Each of the balance signals pass through the Main Interface Chassis and are connected to the Keithley Switch Panel. The switch panel enables the user to select one of the three conditioned balance signals for monitoring while in the Normal operating mode. The output of this switch on cable C56 is connected to the KEITHLEY input connector on the AC CAL Switch Panel.

Regulated AC Supply

A regulated AC power supply is located in the bottom right (front view) of the DSIS cabinet. This unit is used to power the components which would be adversely affected by noisy AC line voltage, such as the PC-386, programmable signal conditioners, and the Q-Flex signal conditioner. Table 5 lists each component in the DSIS rack and its power source.

Power Supply (Sorensen 40-25)

This is a 40 volt, 25 amp power supply that is used to drive the attitude motor. The output is fed through cable C28 into the Motor Speed Controller where it is switched by the attitude Up/Down switch. For the attitude motor, the output is limited to 28 volts.

Cable Descriptions

A listing of all the cables used in the DSIS is provided in table 6. The table lists the cable number, type of connectors, type of cable, starting and terminating destination, the signal that the cable carries, and the length of each cable. The cable number can be cross referenced to figure 1, Cable Layout and Block Diagram. Appendix B contains a drawing for each cable.

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Appendix A

Specifications for specific components of the DSIS are listed below.

PC-386

10 slot passive ISA Bus Backplane Two 5¼" DSDD Floppy Drives 2 serial and 1 parallel port 80 Meg Hard Drive 8 Meg RAM 640x480x256 .28 resolution VGA color monitor 2 in-house developed tachometer related boards DAS-20 Analog and Digital I/O board 16 MHz 80386DX processor 2 DSP-32C boards IEEE-488 board

DSP32C

50 MHz WE DSP32C digital signal processor Zero wait state 64K SRAM 32-bit digital I/O interface bus Two analog input channels with 16 bit sigma-delta converters 64 tap FIR digital anti-alias filters

DAS-20 Board

16 channel, single-ended, multiplexed, 12-bit A/D converter

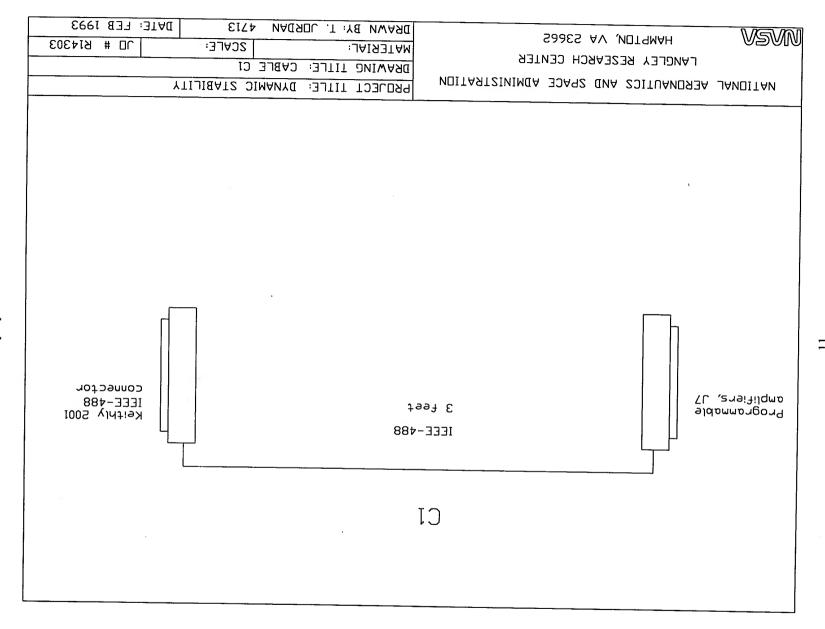
16 digital I/O lines

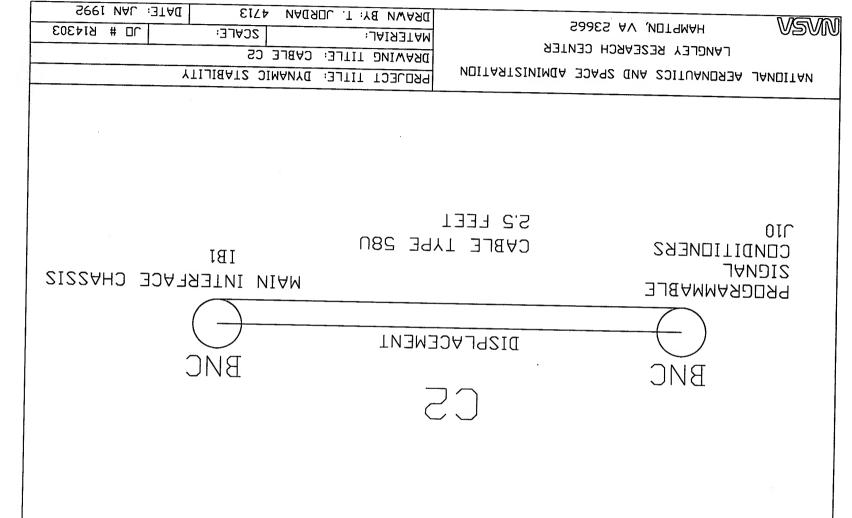
2 channel 12-bit D/A converter

Programmable Signal Conditioners

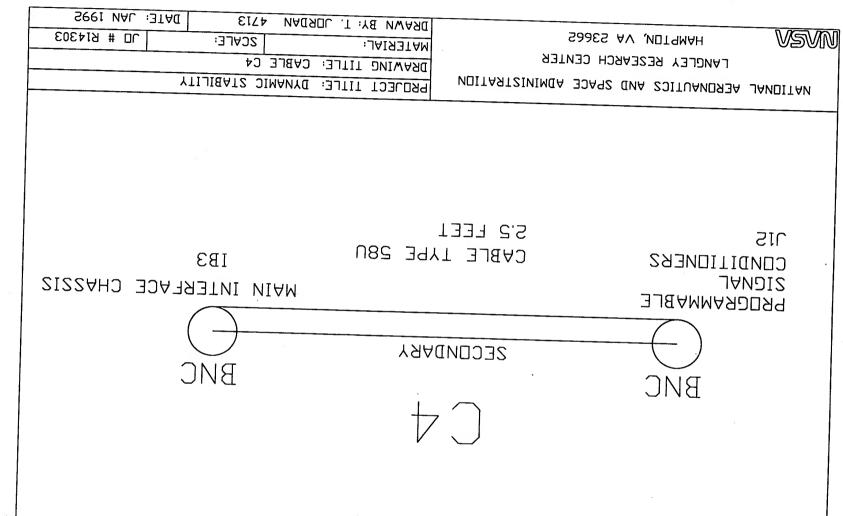
Selectable Gains: 1, 2, 5, 10, 20, 50, 100, 200, 500, 1000, 2000, 5000, & 10000 Anti-Alias Filter: 300 Hz Lowpass Frequency Cutoff, 6-pole Butterworth 500KHz wideband amplification 50M Ohm Input Impedance

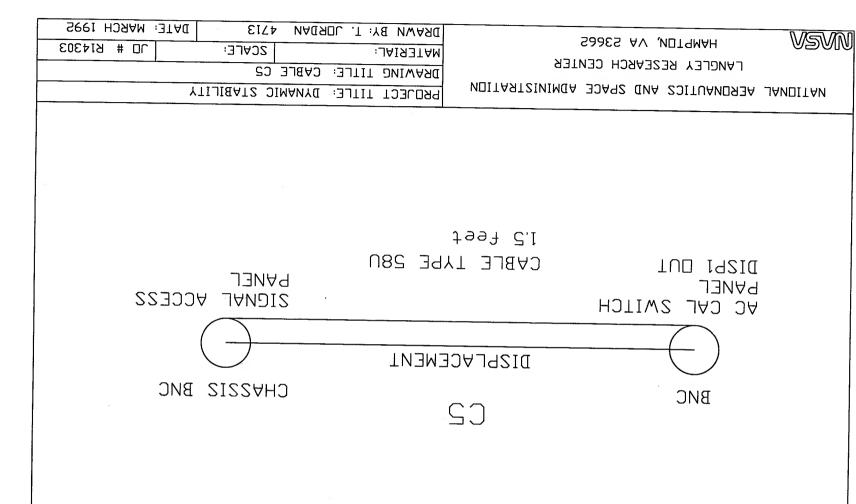
Specific cable drawings are shown below for reference purposes.

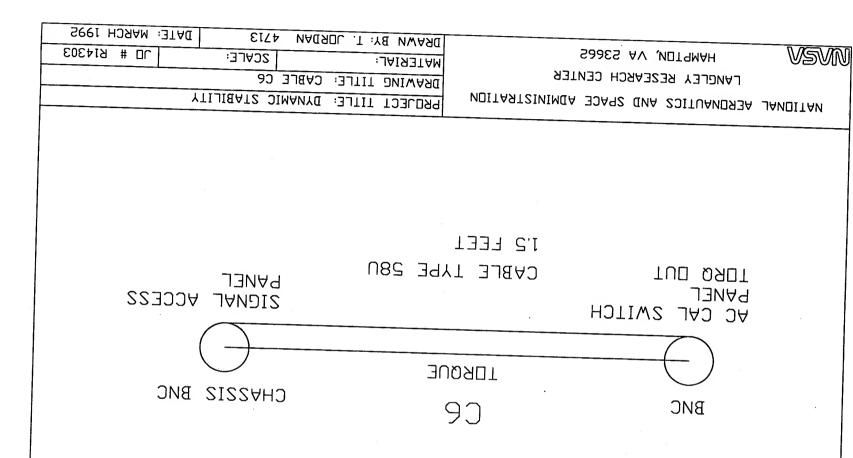


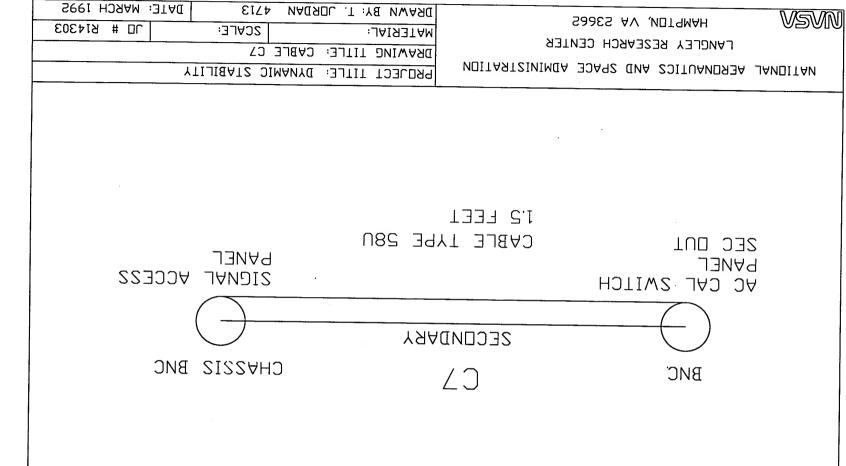


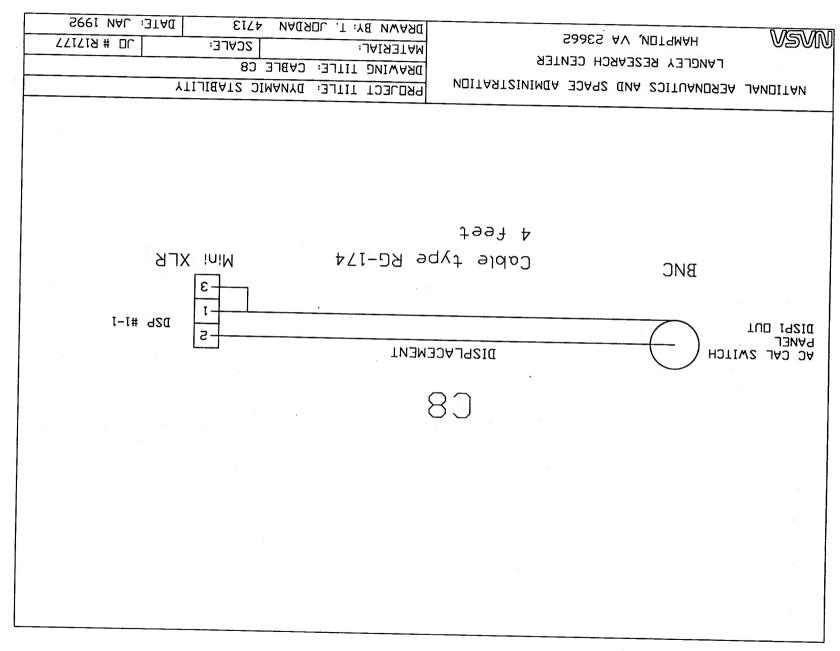
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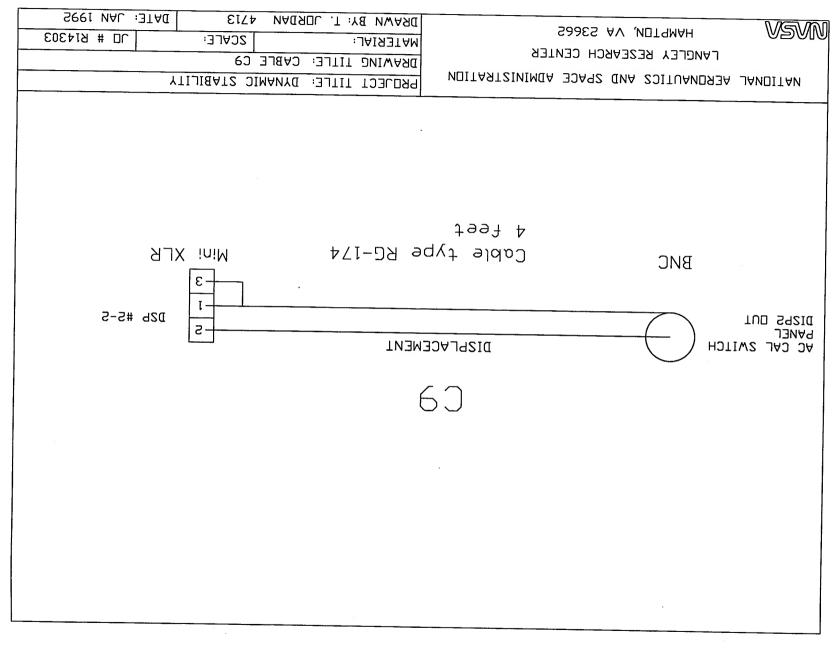




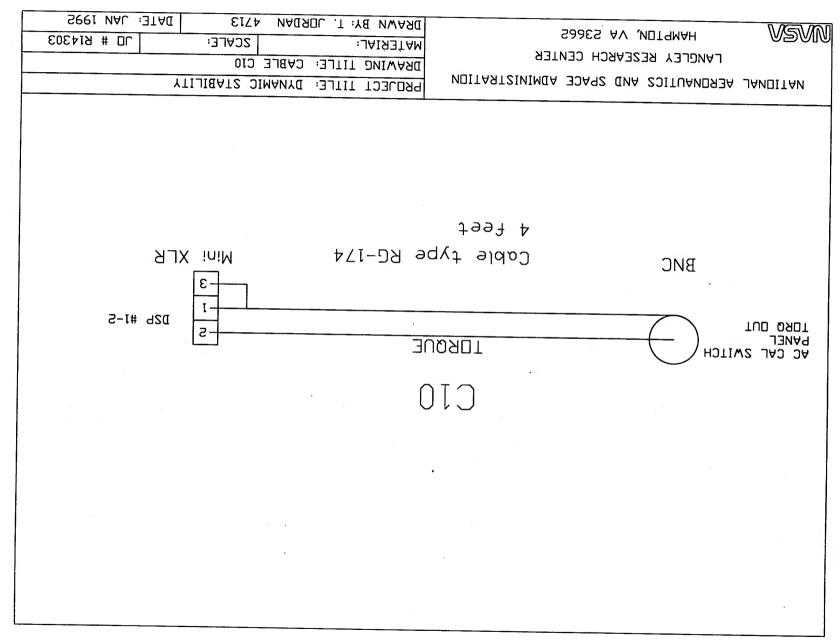


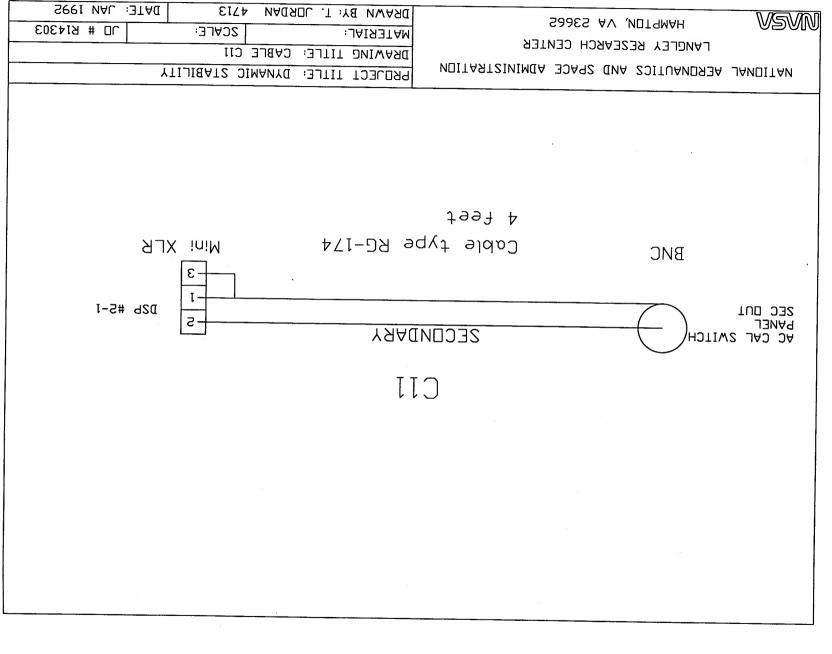


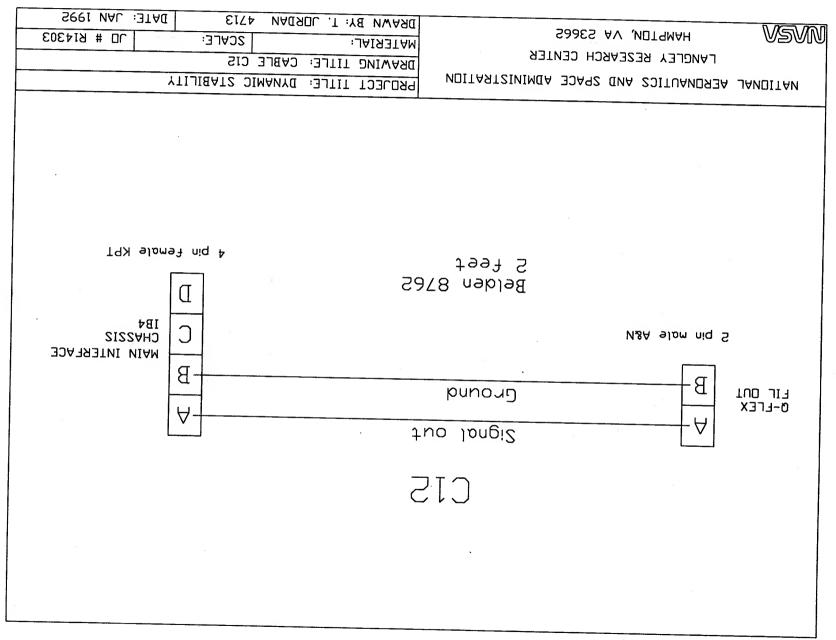


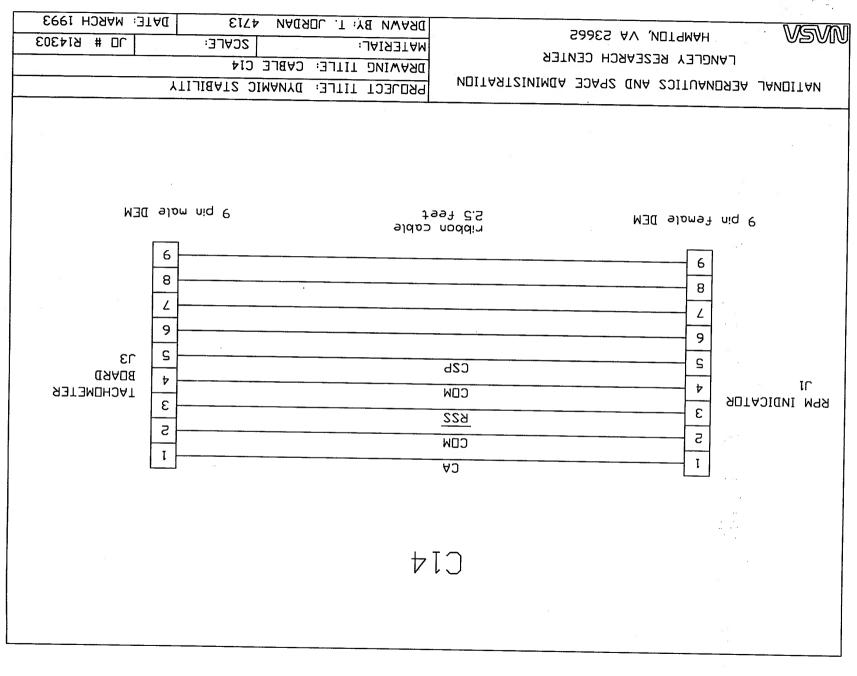




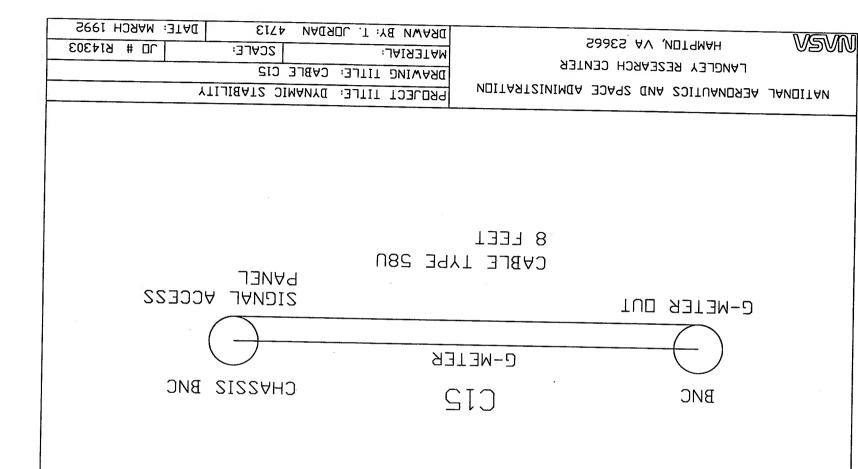


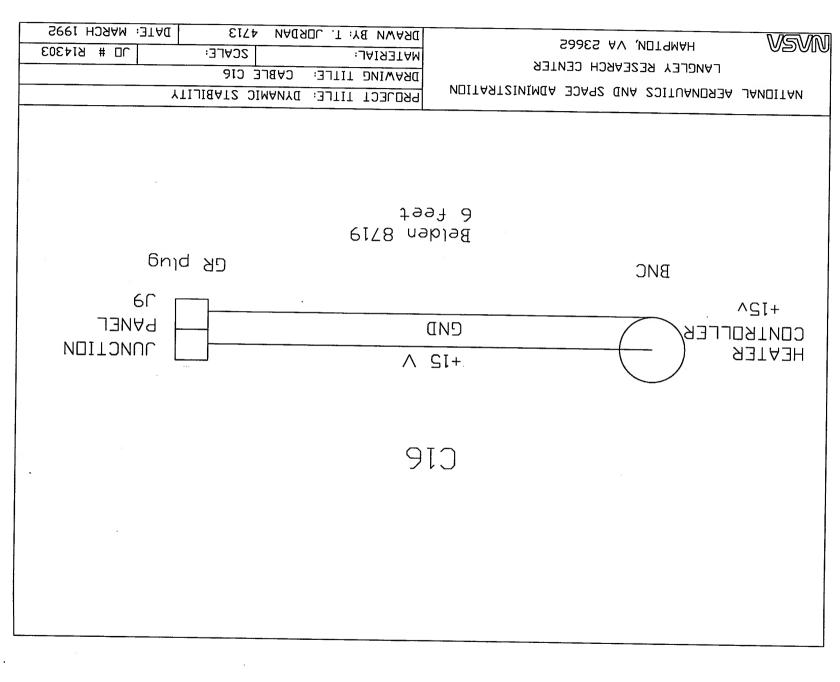




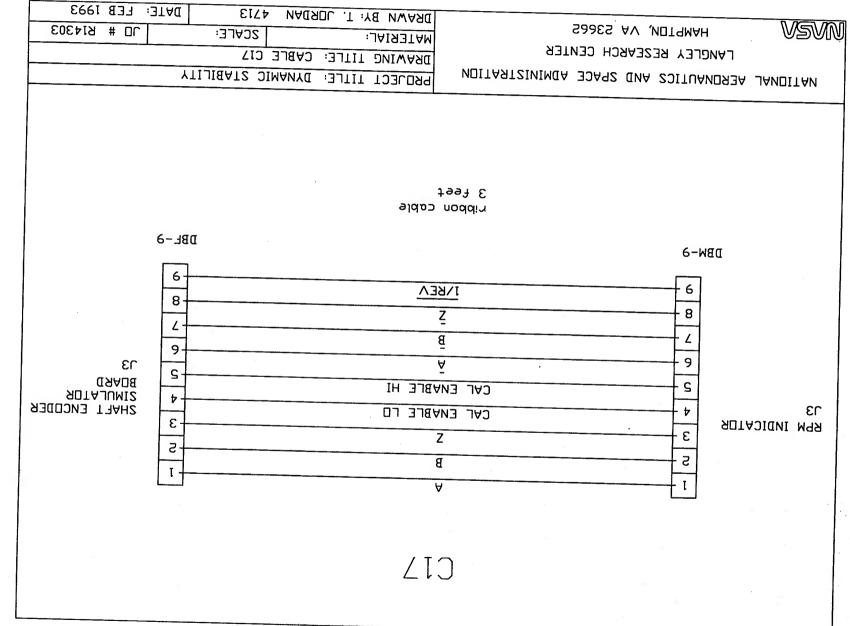


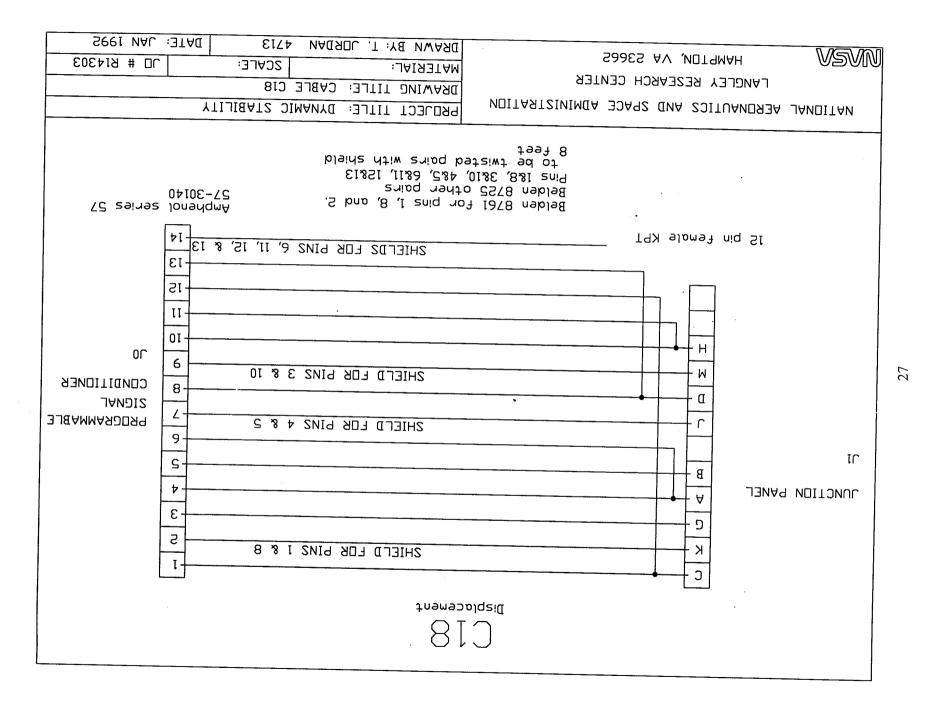


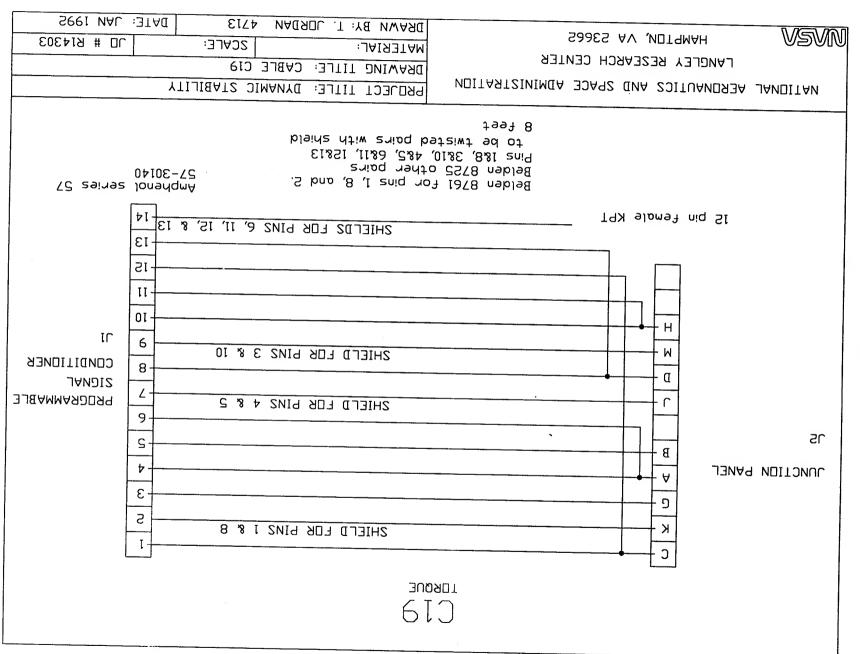




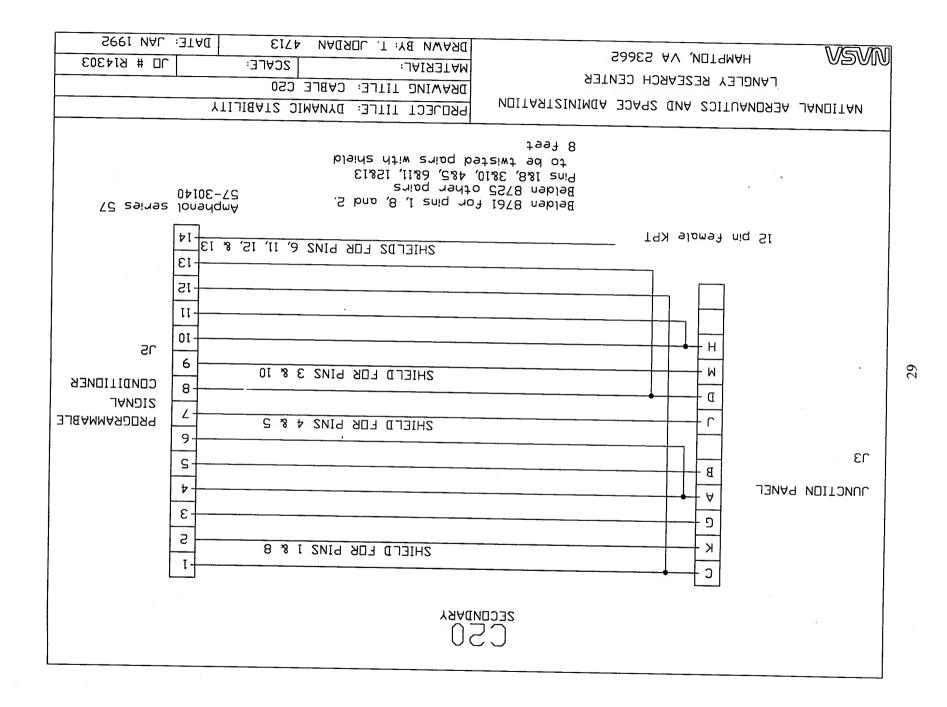


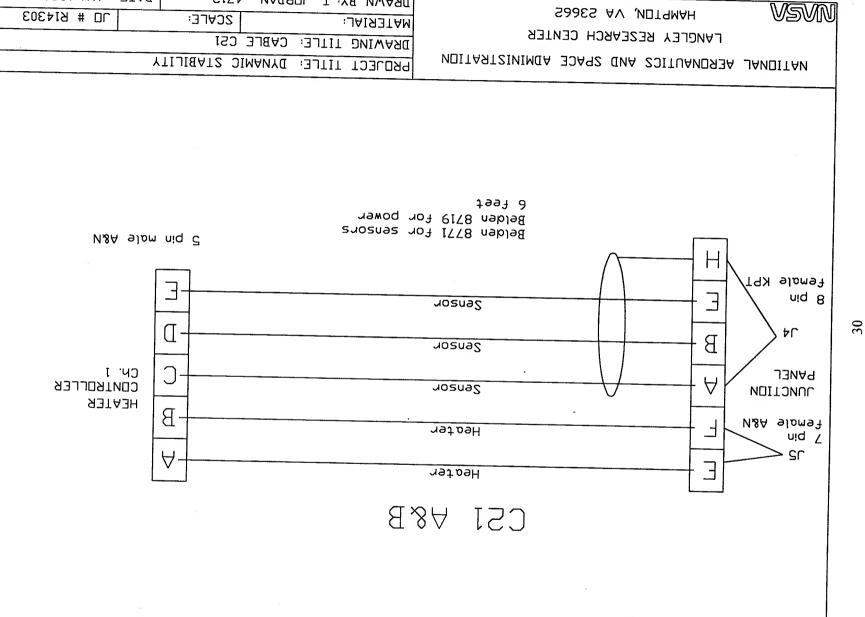






∞

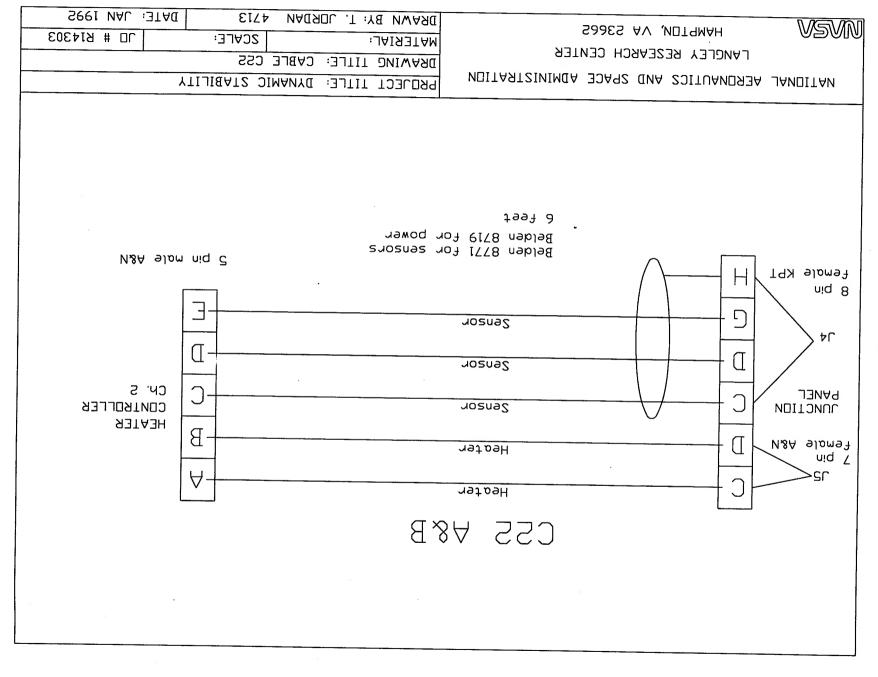


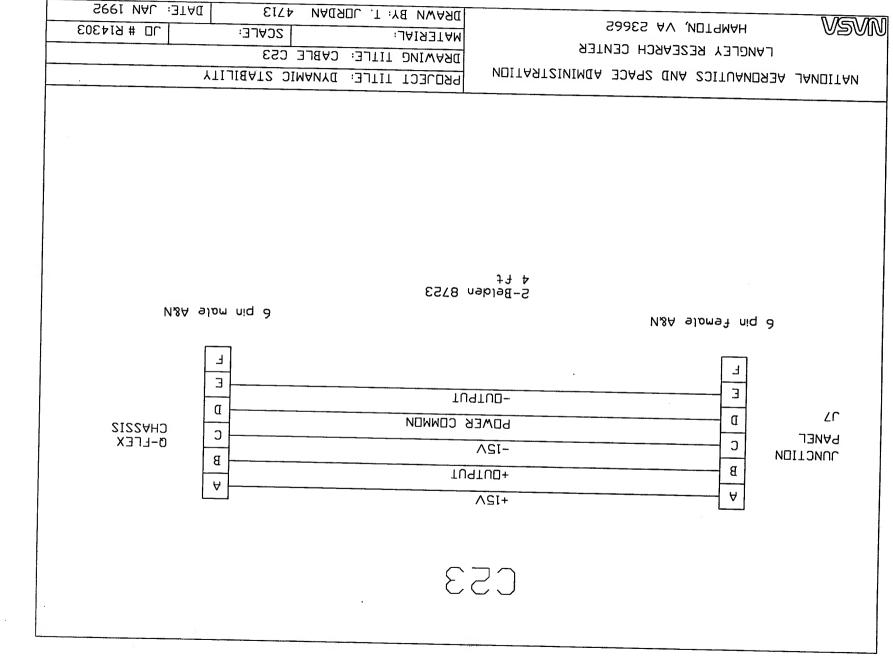


DATE: JAN 1992

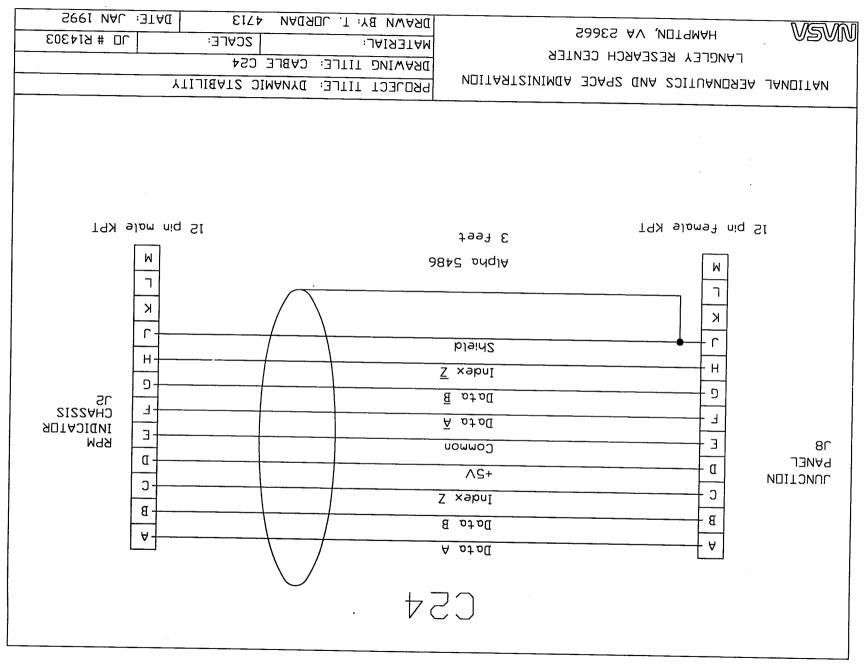
4713

DRAWN BY: T. JURDAN

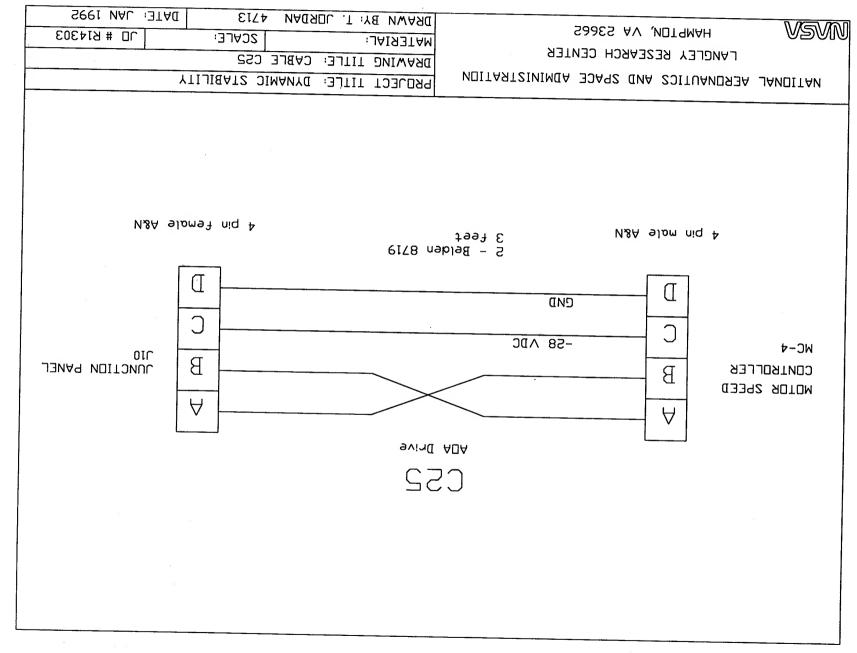




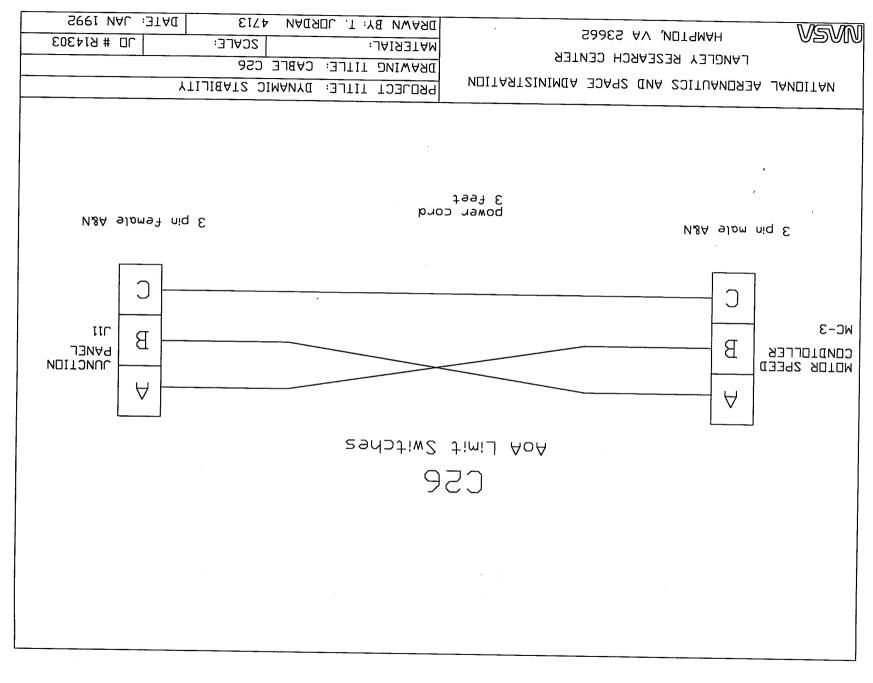




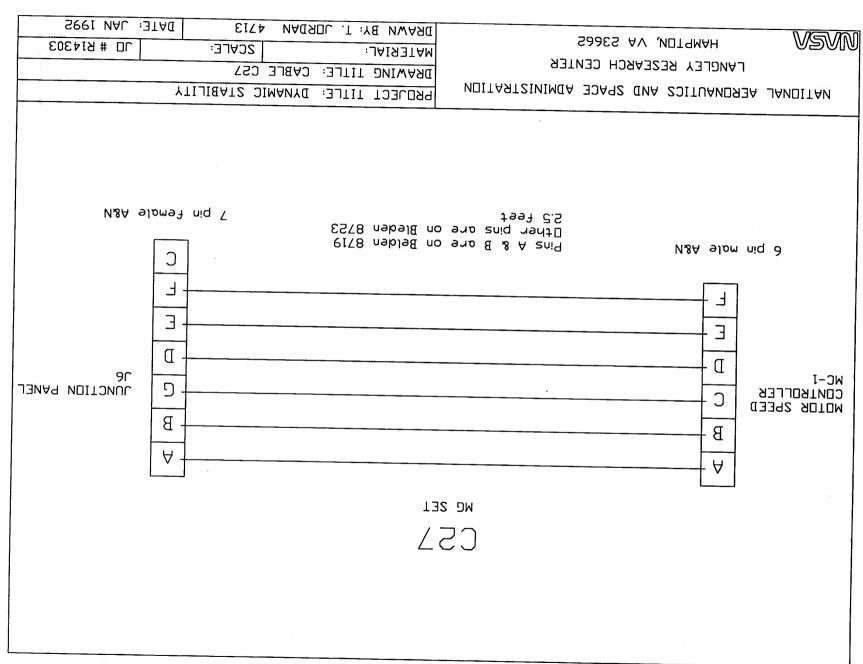












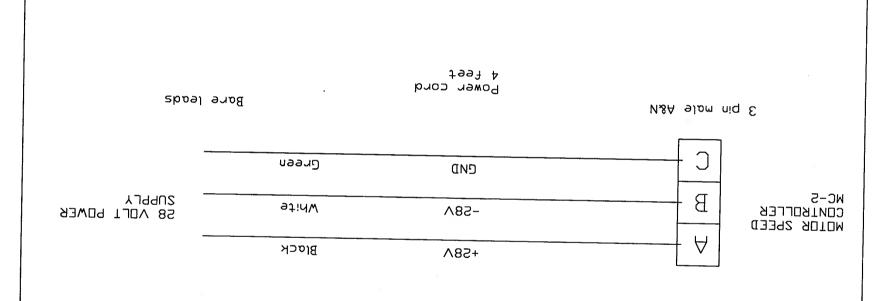
VSVN

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C58



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DRAWING TITLE: CABLE C28

PROJECT TITLE: DYNAMIC STABILITY

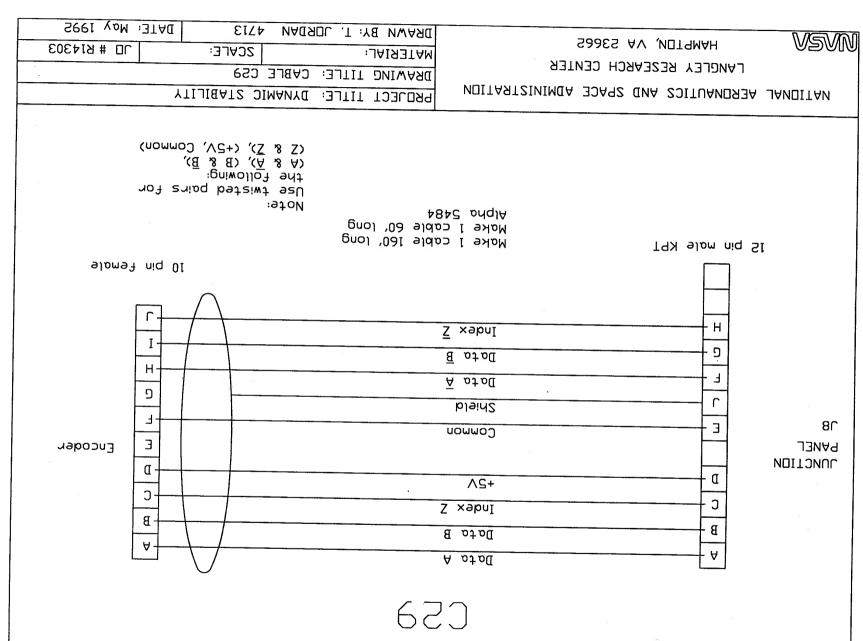
WATERIAL:

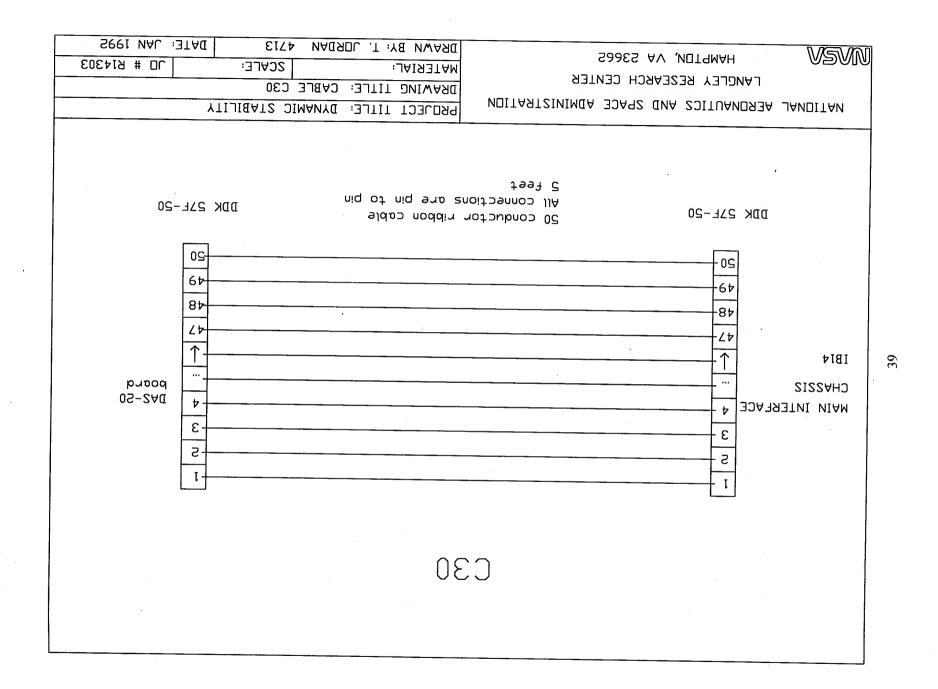
2CALE:

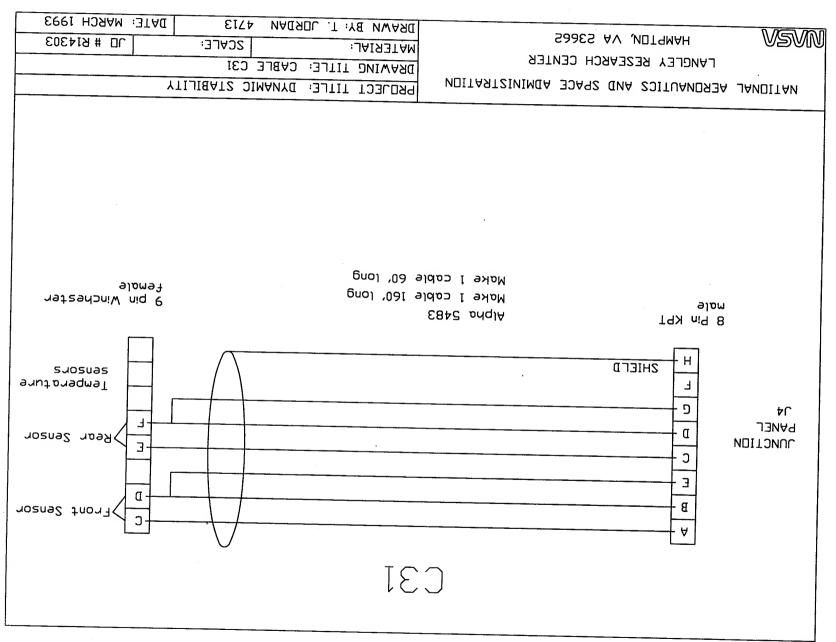
Seel NAL :3TAU

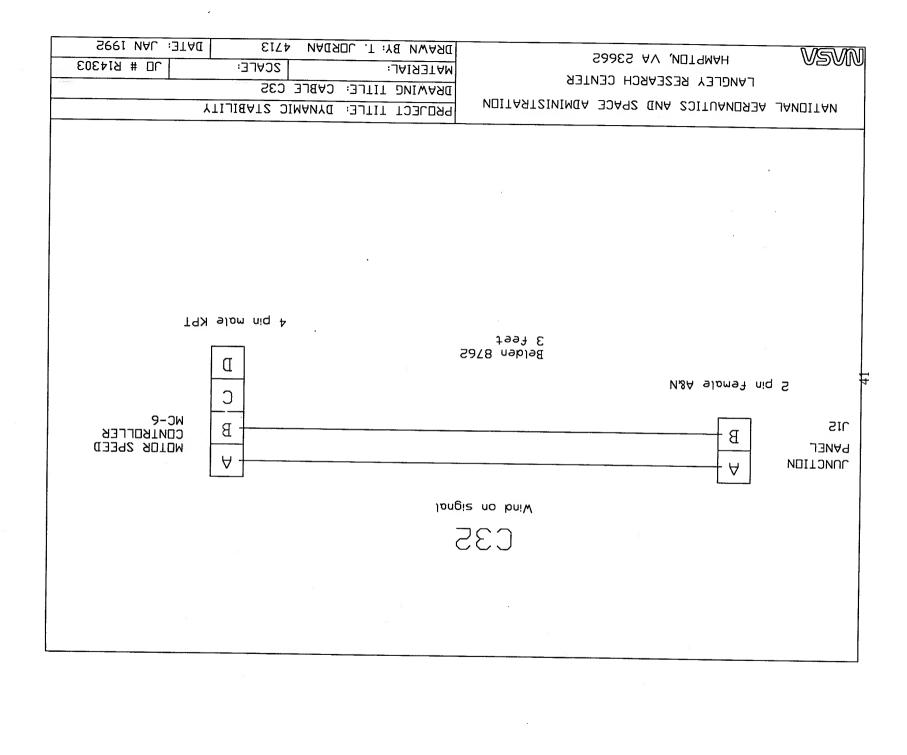
10 # KI4303

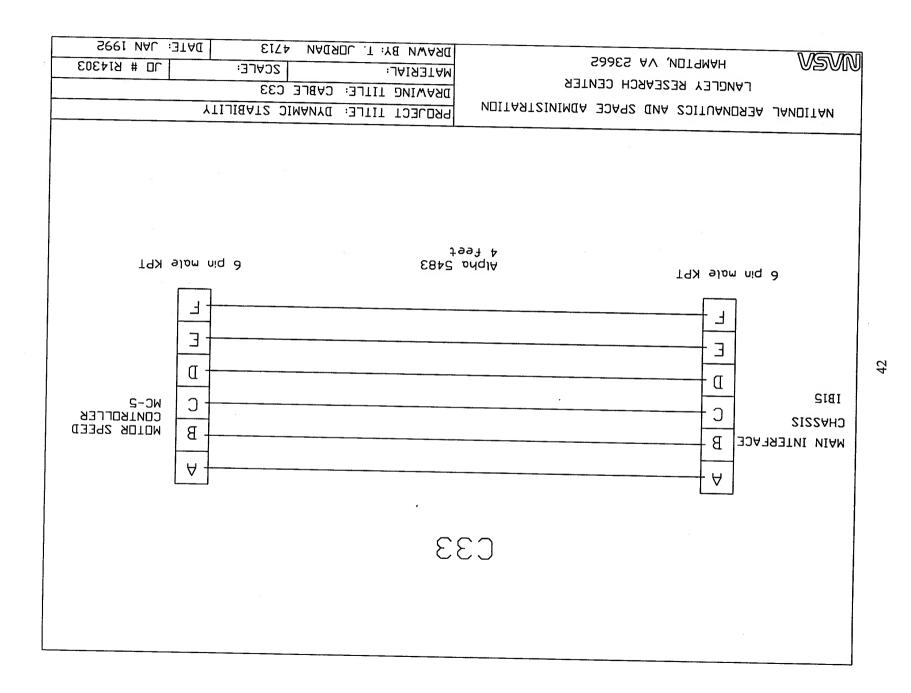












HAMPTON, VA 23662 HAMPTON, VA 23662

NATIONAL AERONAUTICS AND SPACE ADMINISTRATION

PROJECT TITLE: DYNAMIC STABILITY

DRAWING TITLE: CABLE C34

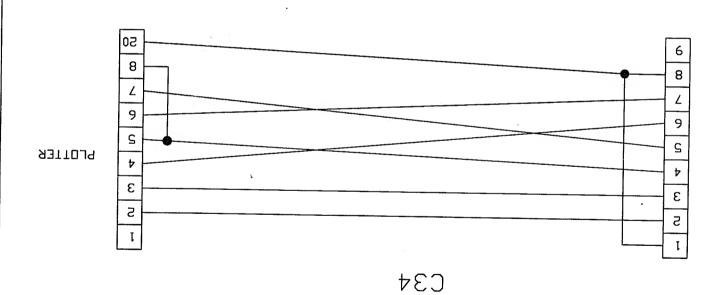
MATERIAL: SCALE: DCALE: M0, 1992

DRAWN BY: T. JURDAN 4713

DATE: M0, 1992

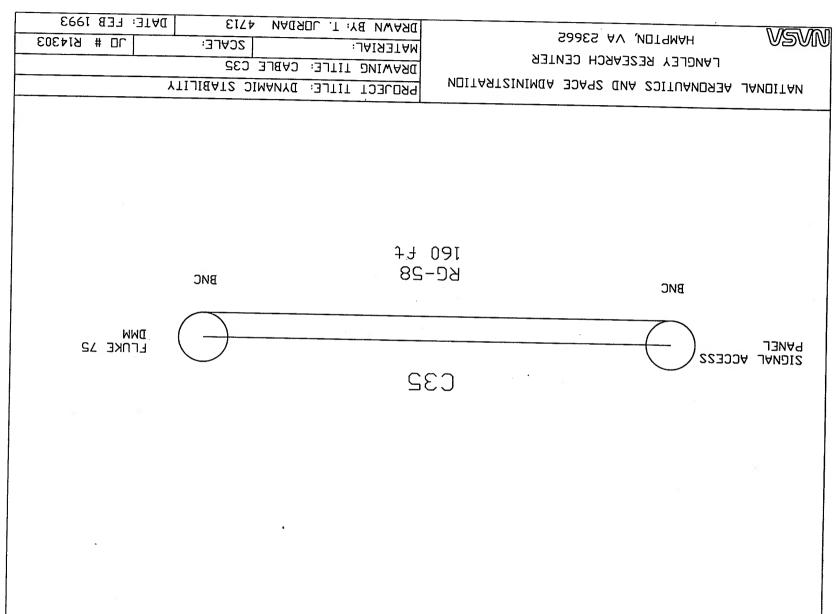
S2 bin wale .D.

9 pin female "D"

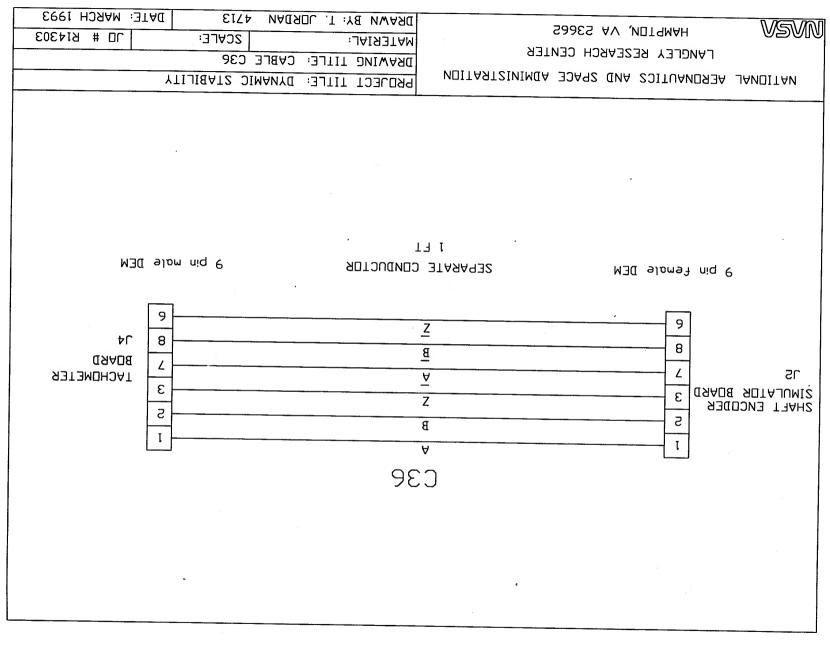


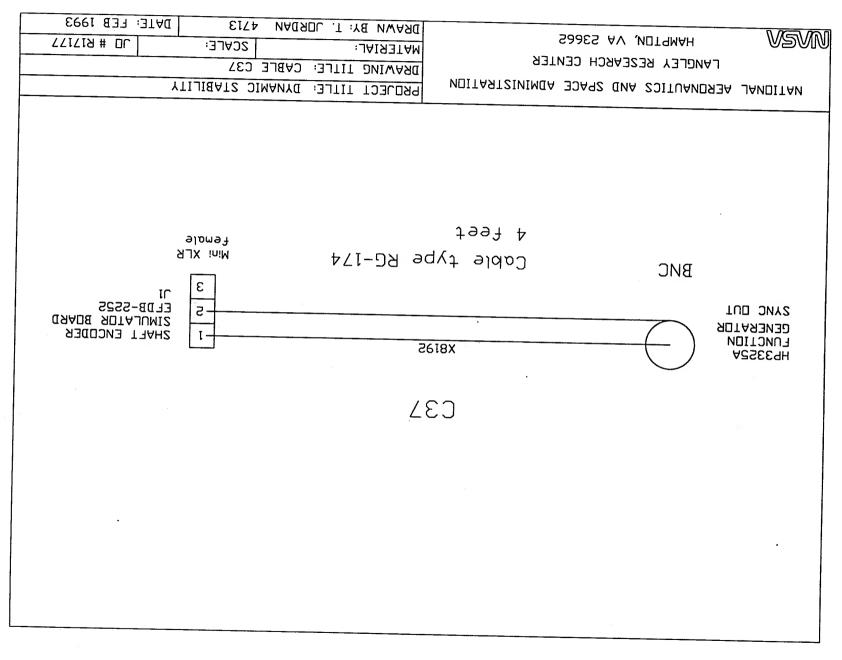
C□W I bc-386 43

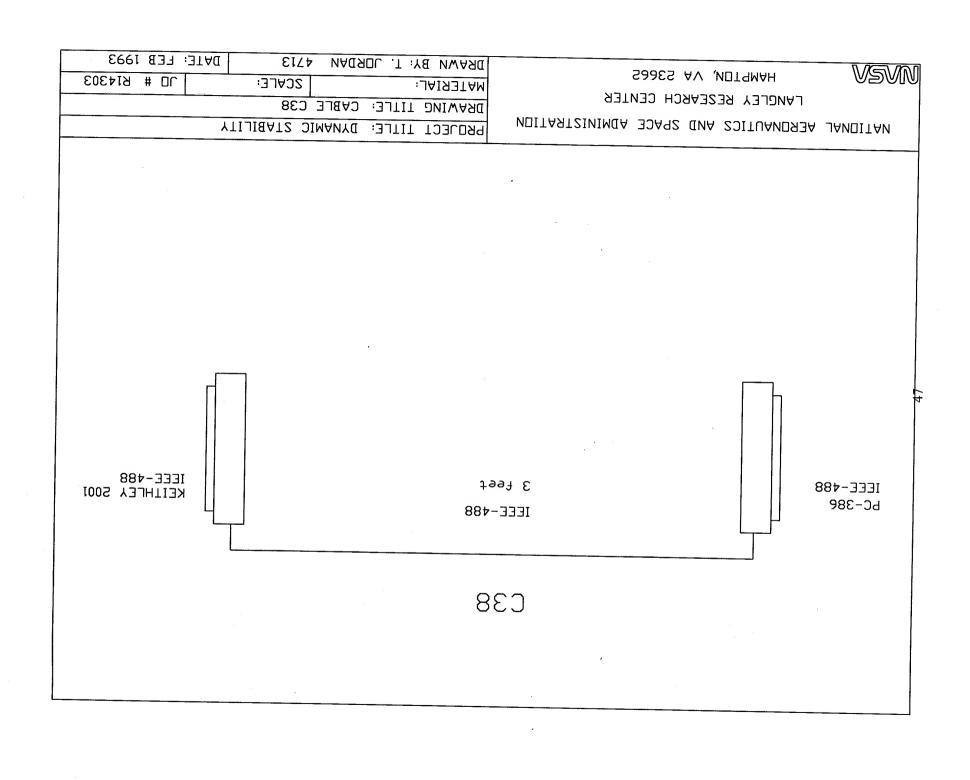










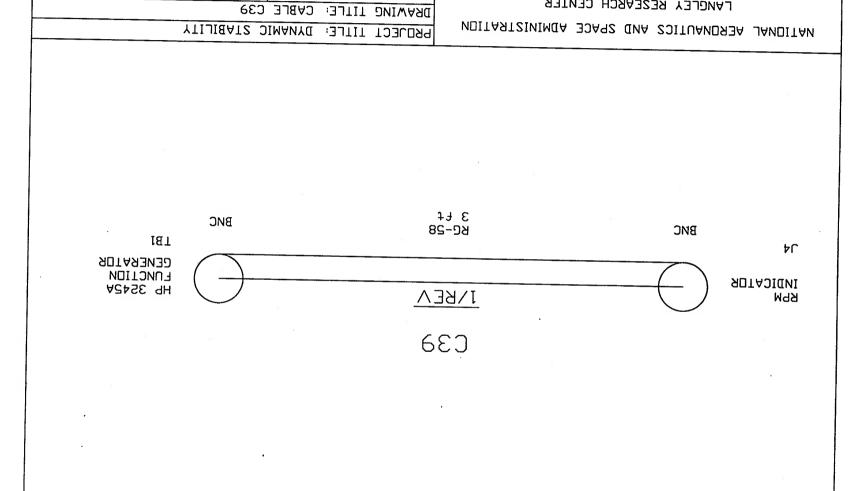




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DATE: FEB 1993

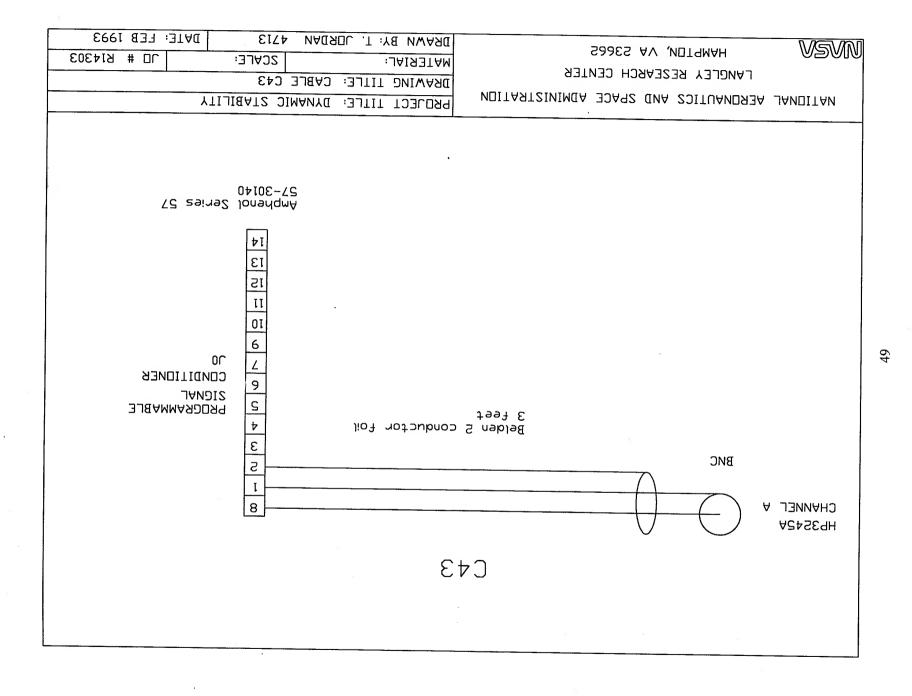
10 # KI4303

4713

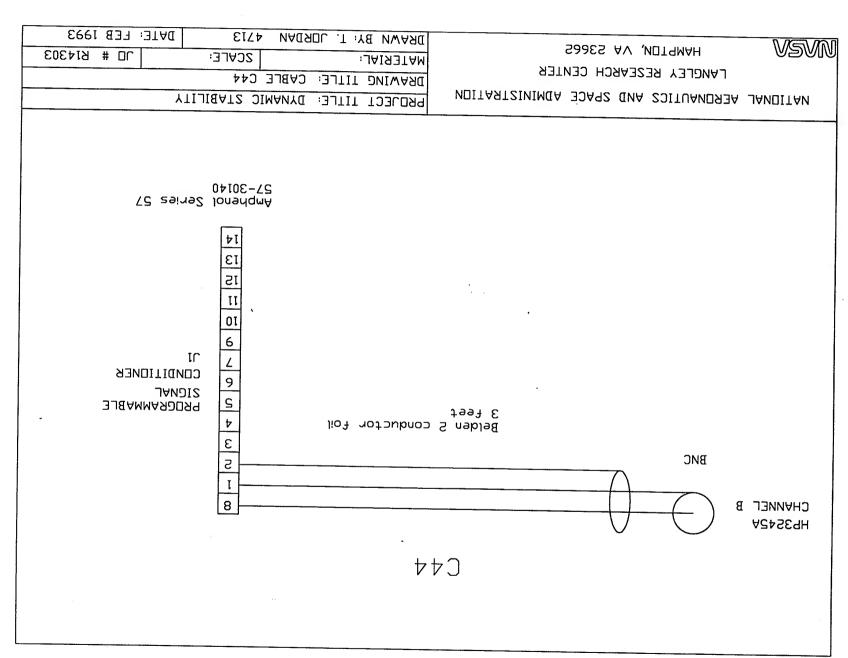
2CALE:

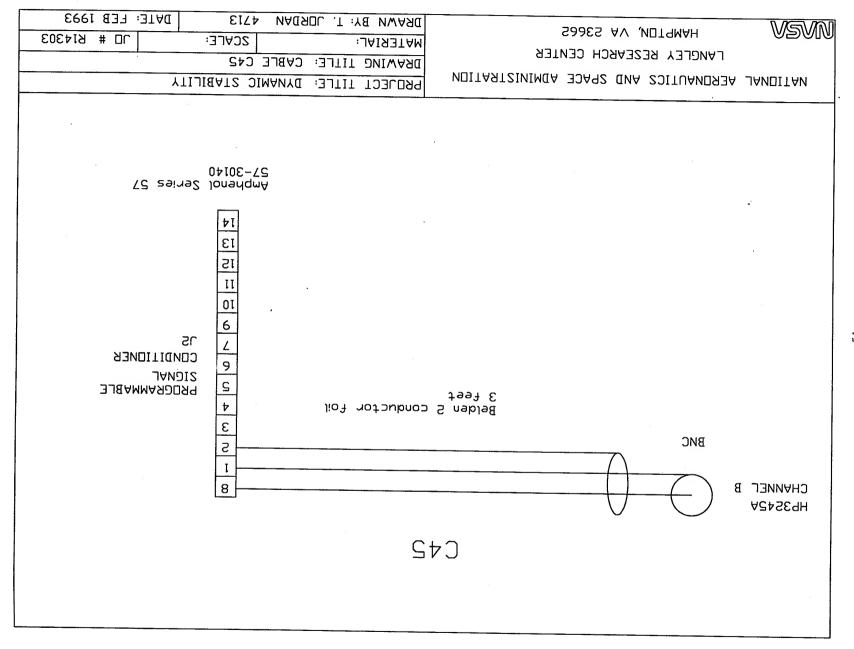
DRAWN BY: T. JORDAN

MATERIAL:









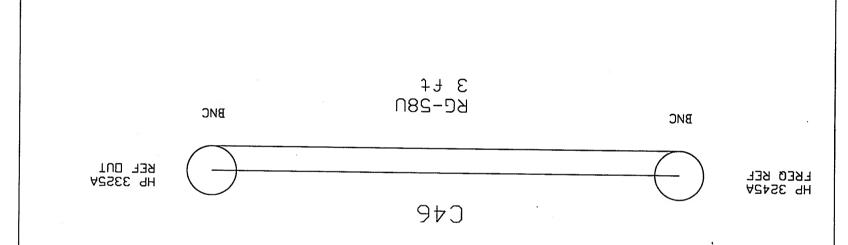


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DATE: FEB 1993

10 # KI4303

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2CALE:

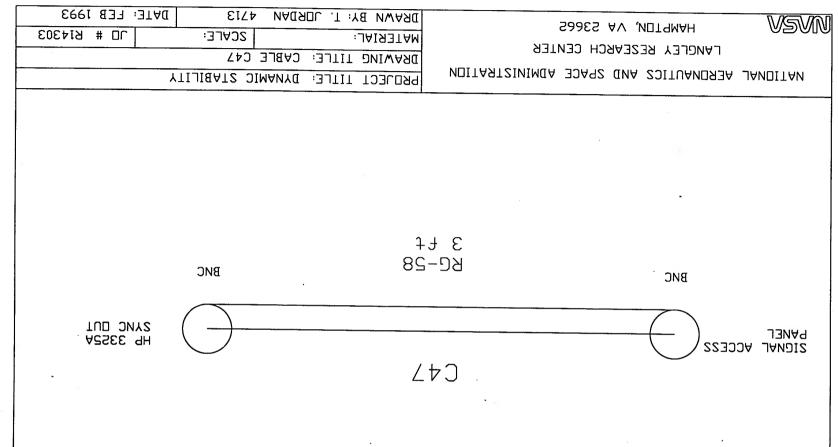
DRAWN BY: T. JURDAN

DRAWING TITLE: CABLE C46

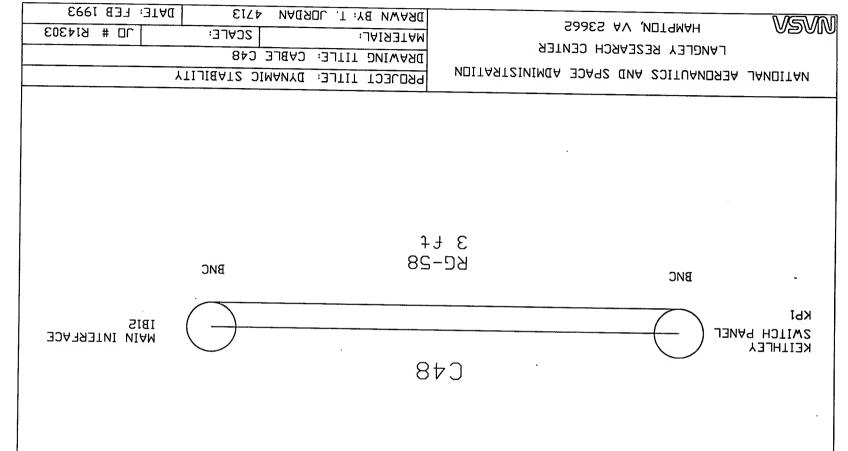
PROJECT TITLE: DYNAMIC STABILITY

MATERIAL:

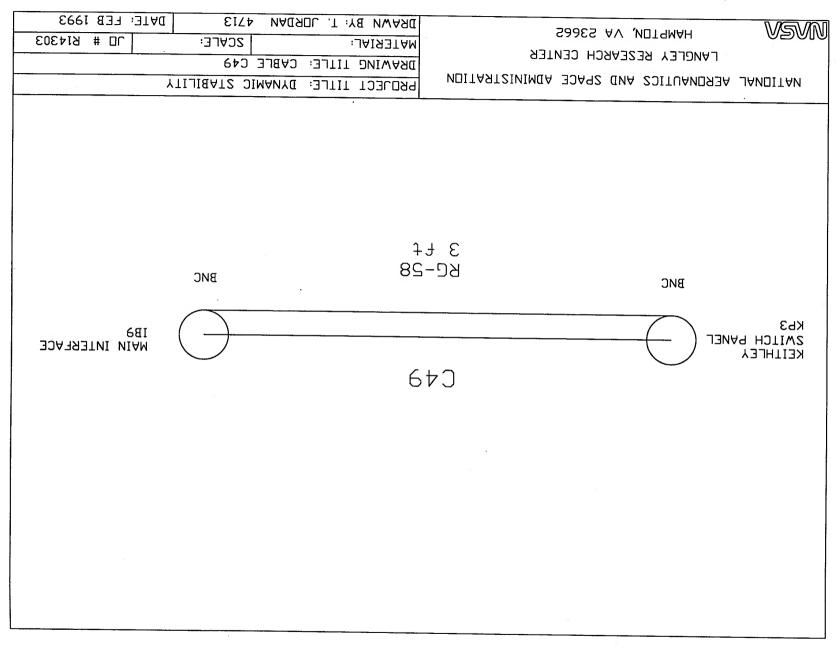


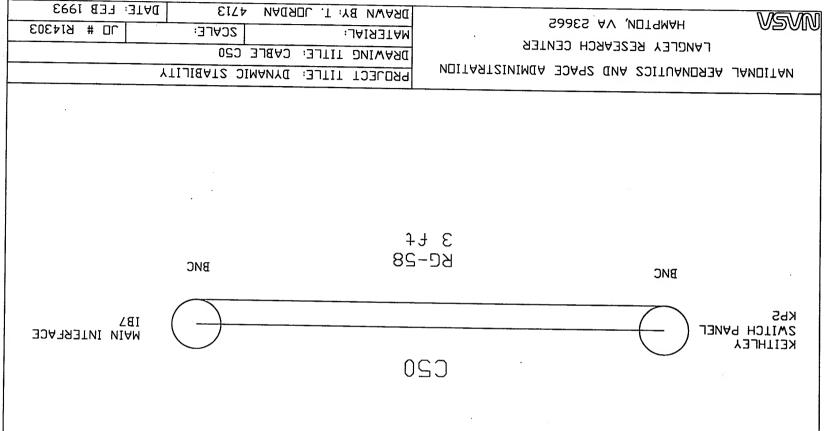




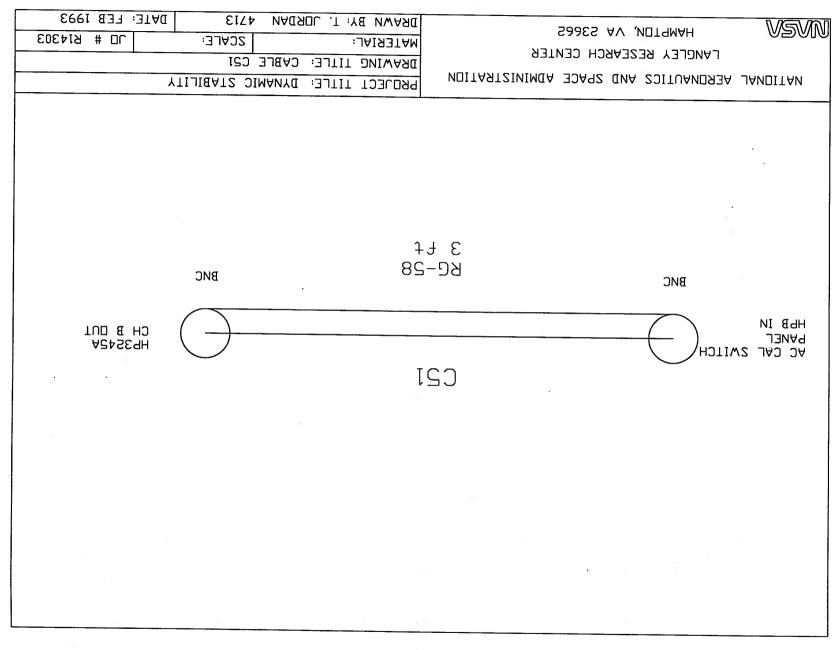


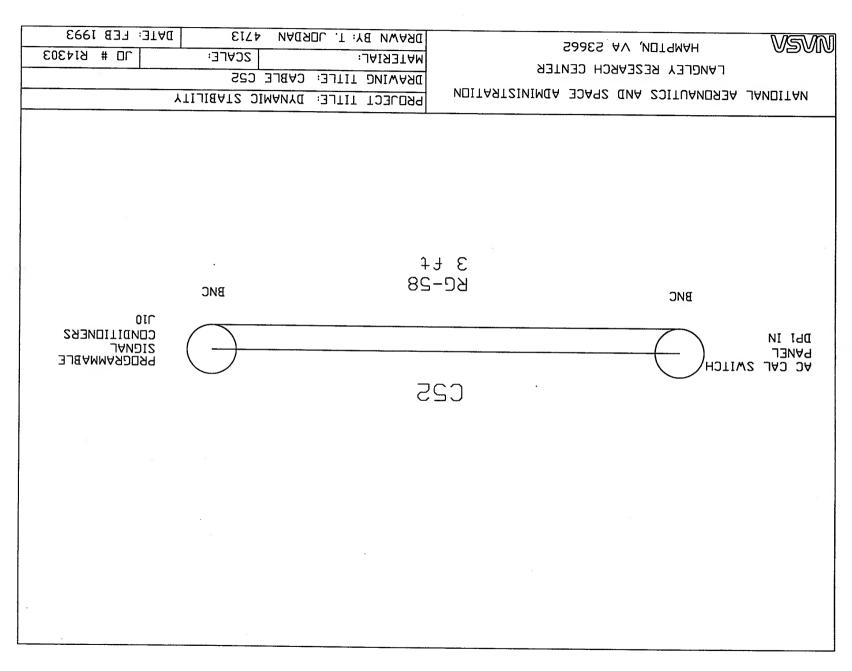


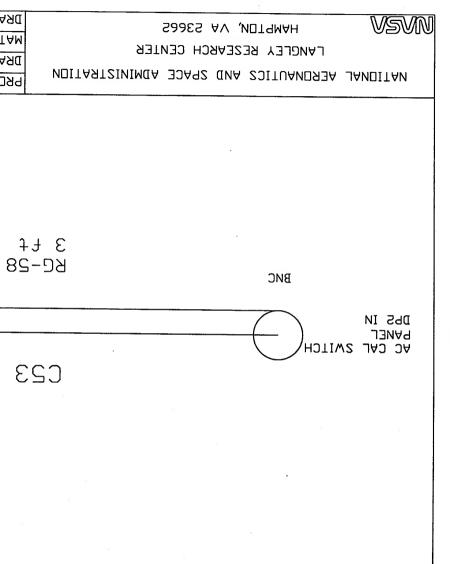












PROJECT TITLE: DYNAMIC STABILITY

DRAWING TITLE: CABLE C53

MATERIAL: SCALE: LEB 1993

DRAWN BY: T. JORDAN 4713

DATE: FEB 1993

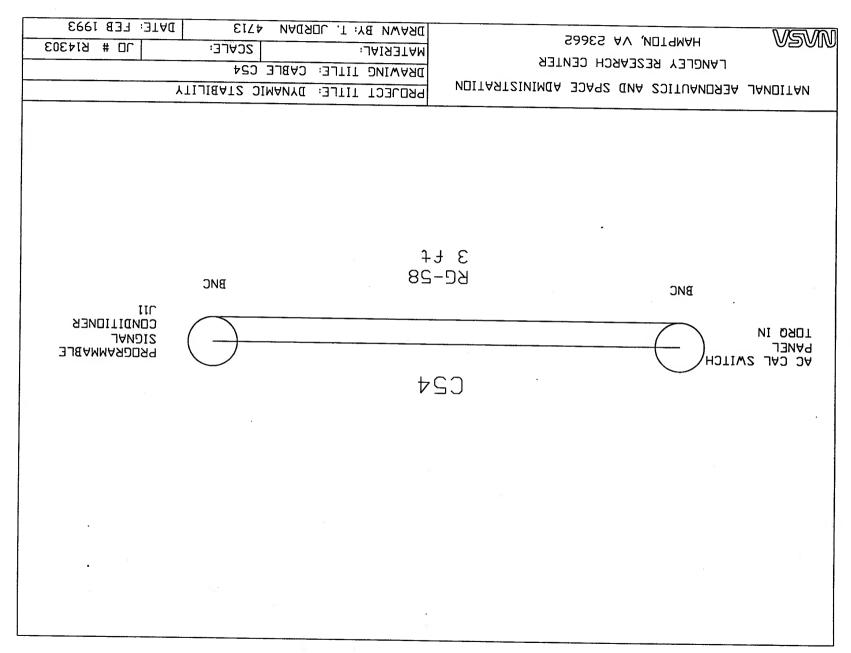
BNC

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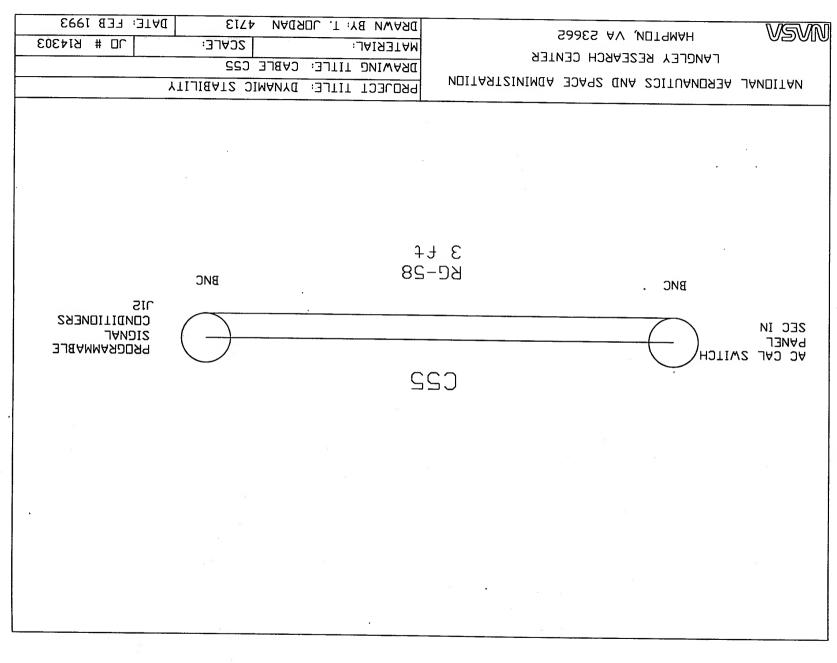
CHASSIS

MAIN INTERFACE

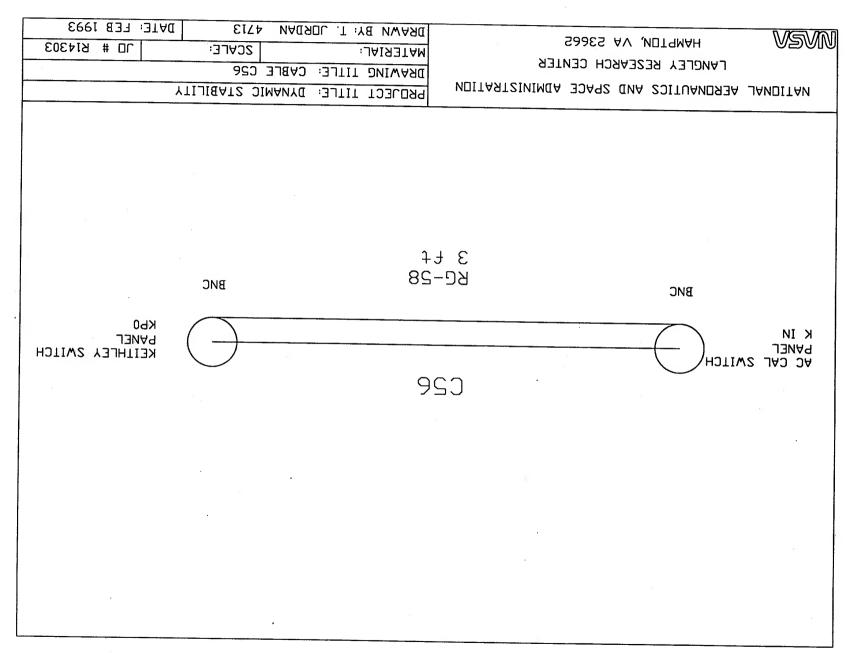
59



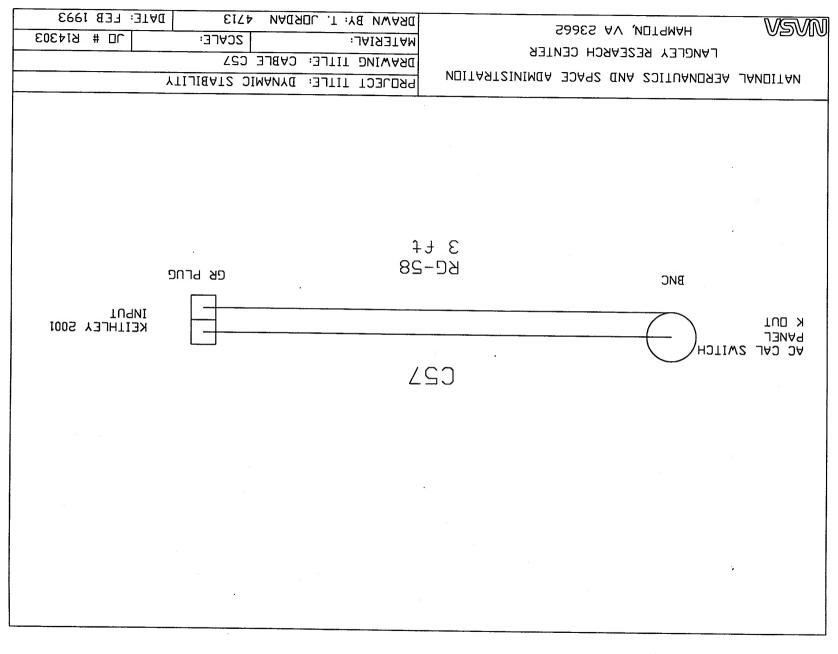




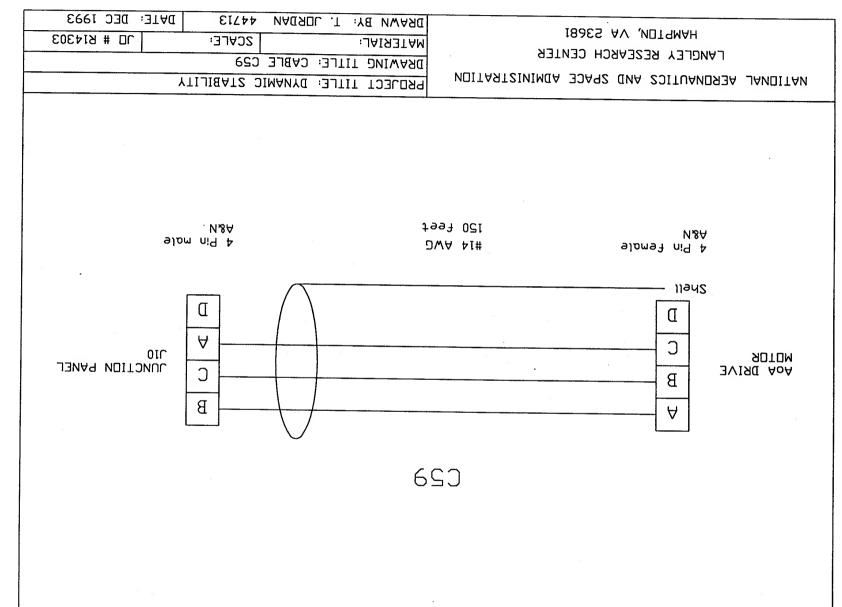


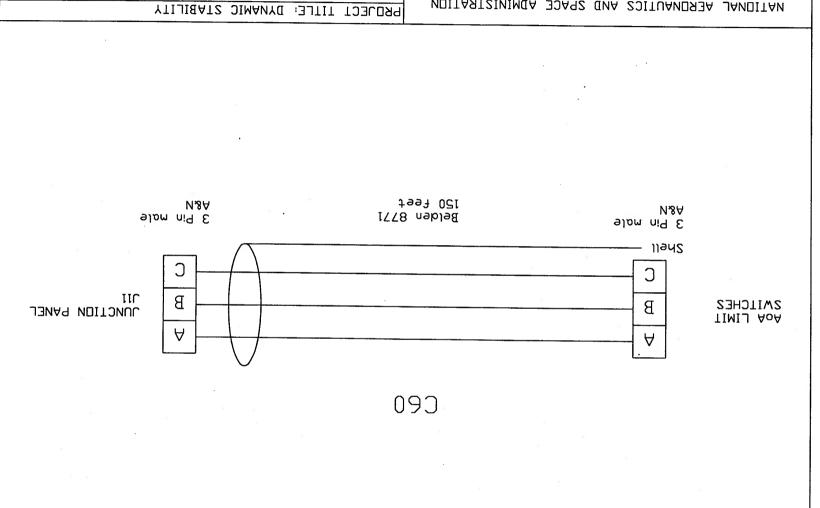












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DRAWING TITLE: CABLE C60

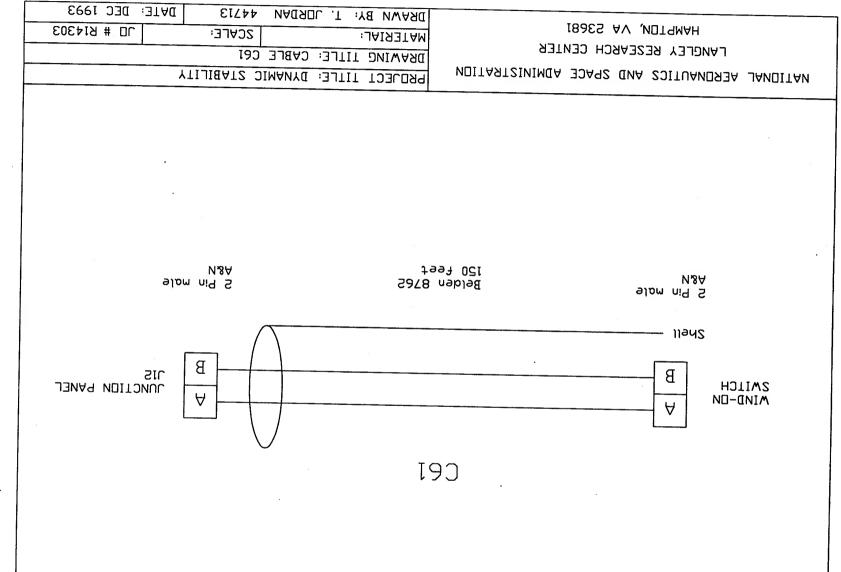
MATERIAL:

2CALE:

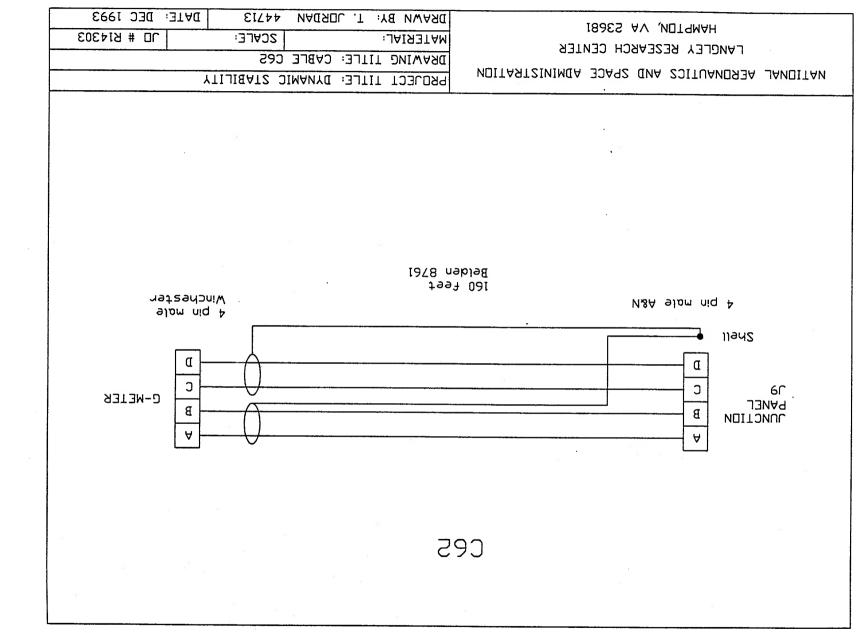
DATE: DEC 1993

10 # KI4303

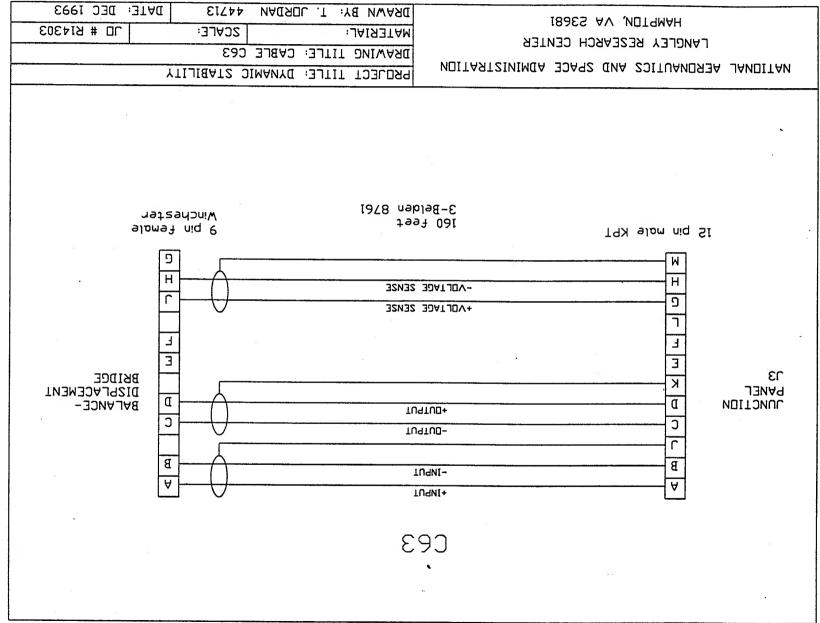




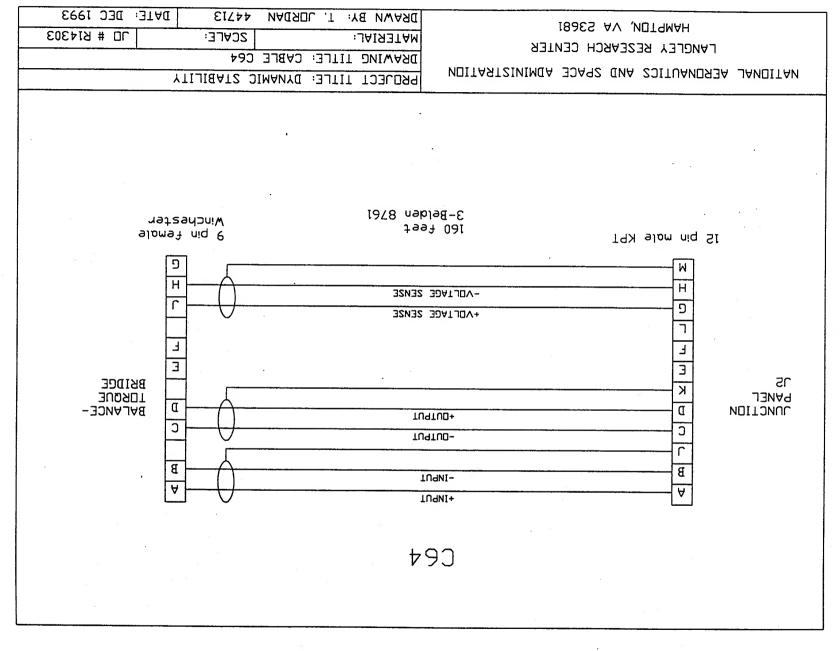




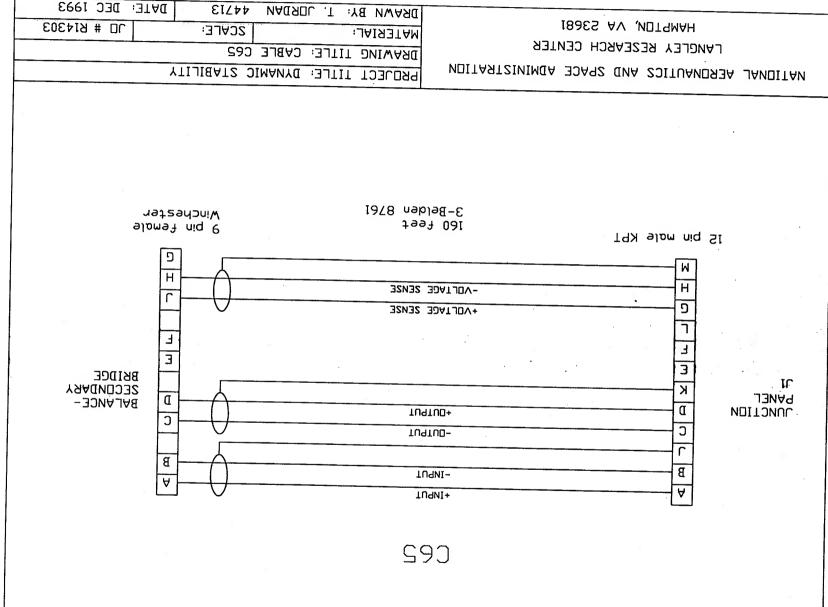










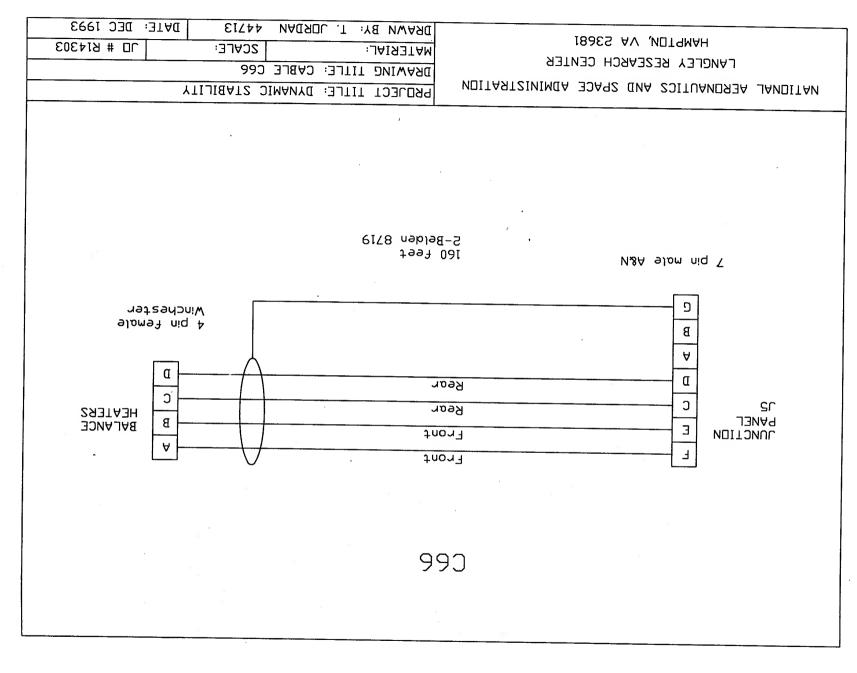


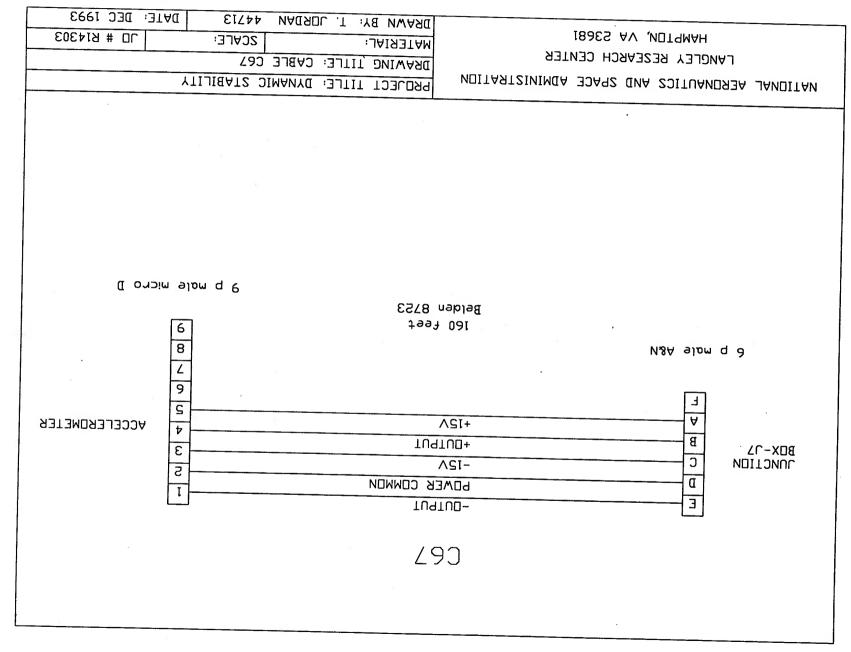
DATE: DEC 1993

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Tables

Table 1. PC Internal Boards

	rinciple Function	N/A	Hard Drive Control	Communications	N/A	Calibration	Measure Balance Oscillation Frequency	N/A	Sample and Process Data		N/A	P.C	Monitor	Process Control	I/O Emotions
Roard Tyne	odti moo	Blank	HD/FD Controller	Serial Port	Blank	Shaft Encoder Simulator Board	Tachometer Board	Blank	DSP#1	DSP #2	Blank	80386 CPU Board	VGA Display Adapter	IEEE-488	DAS-20
PC Backplane Slot		T	2	3	4	. 5	9	7	8	6	10	11	12	13	14

Junction Panel Connectors

Table 2.

	71											
Connector Type	12 Pin Female KPT	12 Pin Female KPT	12 Pin Female KPT	8 Pin Female KPT	7 Pin Female A&N	7 Pin Female A&N	6 Pin Female A&N	12 Pin Female KPT	4 Pin Female A&N	4 Pin Female A&N	3 Pin Female A&N	2 Pin Female A&N
Signal	Displacement	Torque	Secondary	Temperature Sensors	Heaters	Motor-Generator Set	Q-FLEX	Encoder	G-Meter	AoA Motor	AoA Limit Switches	Wind-On Switch
Connector #	J1	J2	J3	J4	JS	J6	17	J8	95	J10	J11	J12

Table 3. Shunt Resistors

Calibrate Signal (millivolts)	17.4	8.7	5.8	4.4
Value (Ohms)	50K	100K	150K	200K
Shunt Pair	1	2	3	4

Table 4. Data Code Descriptions

Data Type.	Calibration Zero	Calibration Data	Tare Zero	Tare Data	Wind-On Zero	Wind-On Data
Data code	.1	6	2	23	9	7

Table 5. Component Power in DSIS Rack

AC Type	Unregulated	Unregulated	Unregulated	Unregulated ,	Unregulated	Unregulated	Unregulated	Regulated	Regulated	Regulated	Regulated	Regulated	Regulated	Regulated	Regulated	Regulated	Regulated (Connected to DC 386)
Component	Heater Controllers	Oscilloscope	Rack Fan #1 & #2	40 VDC Power Supply	XY Plotter	AC Regulator	LaserJet II Printer	PC-386	Programmable Signal Conditioners	Q-FLEX Chassis	Motor Speed Controller and Data Code Display	Main Interface Chassis	HP3245A Function Generator	HP3325A Function Generator	Keithley 2001 DMM	Ono Sokki FFT Analyzer	VGA Monitor

I constit (A)	Length (II)	en En	2.5	2.5	2.5	1.5	1.5	4	4	4	4	7	5	× ×	۰ د	1 V	ی زو	9	9	9	9	9	4 (יז מי	J (2.5	4	160 & 60	5	100 & 60 3	J 4	7	160		† "	n m	en,	ر د	7.7	7 6	ın	3	8	m	. r) (r)	ı m	3	3			150	150	150	160
Signal		amp commands	displacement	iorque secondom	displacement	torque	secondary	displacement	displacement	torque	secondary	Model attitude	rpm into	g-illeter	encoder info	displacement	torque	secondary	power	sensor	power	power & sensor	Model Attitude	AOA motor control	limit switches	MG set	+/- 28V	encoder in & out	data code & AoA	temp sensors wind-on signal	amp & cal res sig	plotter signals	Nulling	encoder	DVM commands	1/REV NOT	Laser Jet	mouse	displacement sim	secondary sim	function gen sync	X8192	displacement	torque	secondary AC Calibration	displacement 1	displacement 2	torque	secondary	Keithley select	video	AoA motor drive	AoA limit switches	wind-on signal	Oisplacement
Type of Cable	vono vonde	IEEE	RG-58U	RG-5811	RG-58U	RG-58U	RG-58U	RG-174	RG-174	RG-174			1100001 P.C. 5811	Relden 8719	ribbon	Belden 5486	Belden 5486	Belden 5486	Bldn 8771	.Bldn 8719	Bldn 8771	Bldn 8719	4-Beiden 8/23	Belden 8719	Power cord	B 8723&8719	Power cord	Alpha 5486	riobon Alnha 5483	Belden 8762	Alpha 5483	RS-232	RG-58U	6 conductor	IEEE .	RG-58U	printer cable	ribbon Boldon 8763	Belden 8762					RG-58U		, -				RG-58U	cable			Belden 8762 v	
To Connector		IEEE	BNC	BNC	chassis BNC	chassis BNC	chassis BNC	mini XLR	mini XLR	mini XLK	mini ALK 4 nin famela VDT	o nin mole DEM	chassis BNC	GR plug	9 pin DBF	14 pin flat male	14 pin flat male	14 pin flat male	5 pin male A&N	5 pin male A&N	5 pin male A&N	5 pin male A&N	12 nin male KPT	4 pin male A&N	3 pin male A&N	6 pin male A&N	3 pin male A&N	10 pin female 50 pin microribbon	8 pin male KPT	4 pin male KPT	6 pin male KPT	25 pin male DBM	BNC grift Press	9 pin Dam BNC	IEEE	BNC	parallel	Integral	BNC	BNC	BNC	BNC	BNC	BNC	BNC	BNC	BNC	BNC	BNC	GR plug	9 pin DBM	4 pin male A&N	3 pin male A&N	2 pin male A&N 4 pin male A&N	12 pin male KPT
From Connector		IEEE	BNC	BNC	BNC	BNC	BNC	BNC	BNC	BNC	2 nin male A&N			BNC	9 pin DBM	12 pin female KPT	12 pin female KPT	12 pin female KPT	8 pin female KPT	/ pin temale A&N	8 pin temaleK.P.I.	/ pin femaleA&N	12 pin female KPT	4pin female A&N	3 pin female A&N	7 pin female A&N	Screw Terminal	12 pin male KP1 50 nin microribhon	9 pin female Win	2 pin female A&N	6 pin female KPT	9 pin female DBM	chasis BNC	mini XLR	IEEE			9 più DBr 14 nin flat male		ı flat male			BNC						BNC			z		4 pin male Win 4	
To	;	prog. amp-J7 main interface-IR1	main interface-IB2		t signal access panel	A Cal sp-1 ORQ out signal access panel	signal access panel	1 DSF #1-1	1 DSP #1-2	DSP #2-1	main interface-IB4	Tachometer board-13	signal access panel	junction panel-19	SESB-13	prog sig cond-J0	prog sig cond-J1	prog sig cond-J2	heater cutrir-front	heater cutrir-iront	heater cutrir-rear	O-FLEX Chassis	RPM indicator-J2	mtr spd cntrlr-MC4	mtr spd entrir-MC3	mtr spd cntrlr-MCI	mtr spd entrir-MC2	DAS-20Board	junction panel-J4	mtr spd cntrlr-MC6	mtr spd cntrlr-MC5	plotter-serial in	riuke 73 Tachometer board	HP 3325A sync out	Keithley 2001	HP 3245A-TB1	Laser Jet	HP 3245A-ch A out	HP 3245A-ch B out	Ħ		Hr 3323A sync out					10		FINE SIE CONG-112 I	2001-input		_	Junction panel-J11 3		
From	Voithlan 2001	neithey 2001 prog sig cond-J10	prog amp-J11	prog sig cond-J12	AC cal sp-DISP1 out	AC cal sp-1 OKQ ou	AC cal en-DISD1 em	AC cal sp-DISP2 out DSP #1-1	AC cal sp-TORO out DSP #1-2	AC cal sp-SEC out	Q-Flex-Fil Out	RPM indicator-J1	g-meter out	heater cntrlr +15v	RPM indicator-J3	Junction panel-J1	Junction panel-12	junction panel-13	Junction panel-14	imperion panel-14	iunction panel-15	junction panel-J7	junction panel-J8	junction panel-J10	unction panel-J11	Junction panel-16	20 v p.s. Innction nanel-18	main interface-IB14	temp sensors	junction panel-J12	main interface-IB15	r380 COMI	SESB-12	SESB-J1	EEE-488 card	RPM indicator-J4	•	0				KSP-KPI						AC cal sp-1 OKQ in p			-	AoA drive motor ji	-,	ے. ر	Displacement bridge ju
Cable #	5	5 5	3	2 8	ර ර	3 5	S &	3 ව	C10	C11	C12	C14	C15	CI6	CI)	2 S	S C	C20	C218	C22.A	C22B	C23	C24	C25	38	3.5	C29	•		·		-		C37 s	,			C43 p				- /-										C59			

Table 6. Continued

160 160 160 160 150
Torque Secondary Heater power Model attitude Motor Control
Belden 8761 Belden 8761 Belden 8719 Belden 8723 Belden 8771
12 pin male KPT 12 pin male KPT 7 pin male A&N 6 pin female A&N 7 pin male A&N
9 pin female Win 12 pin male KPT 9 pin female Win 12 pin male KPT 4 pin female Win 7 pin male A&N 9 pin male Micro-D 6 pin female A&N 7 pin female A&N 7 pin male A&N
junction panel-J2 junction panel-J1 junction panel-J5 junction panel-J7 junction panel-J7
Torque bridge Secondary bridge Heaters Accelerometer M-G Set
C64 C65 C66 C67 C68

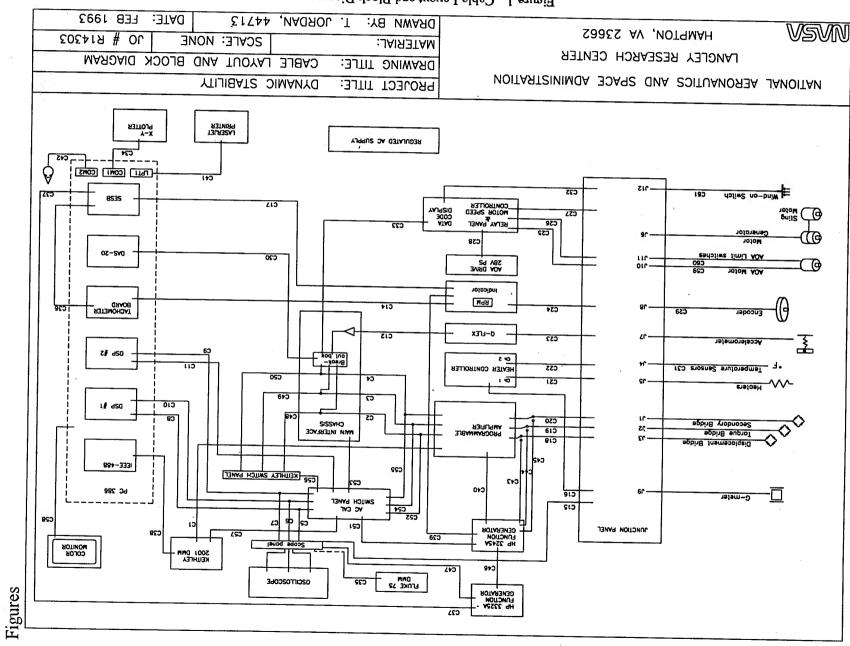


Figure 1. Cable Layout and Block Diagram

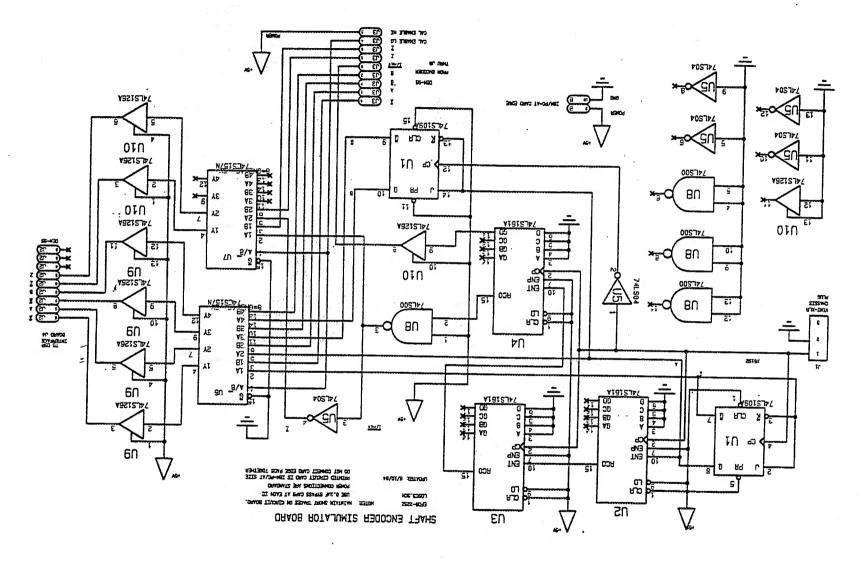


Figure 2. Shaft Encoder Simulator Board

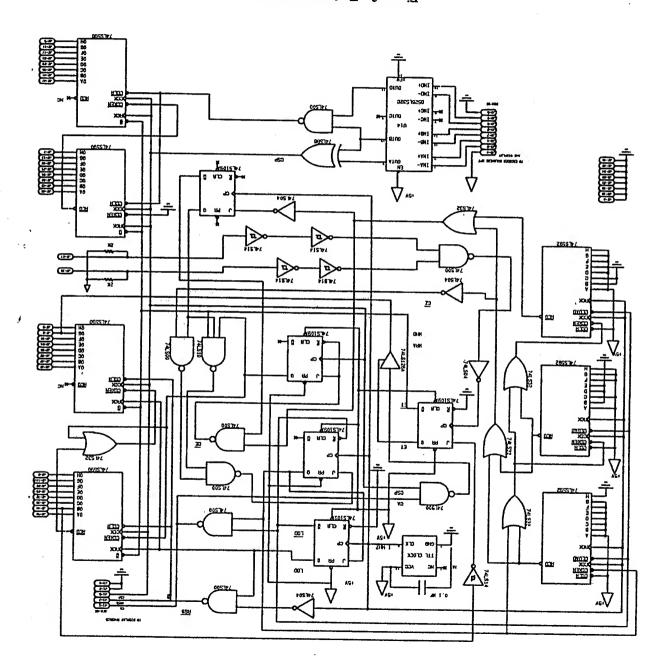
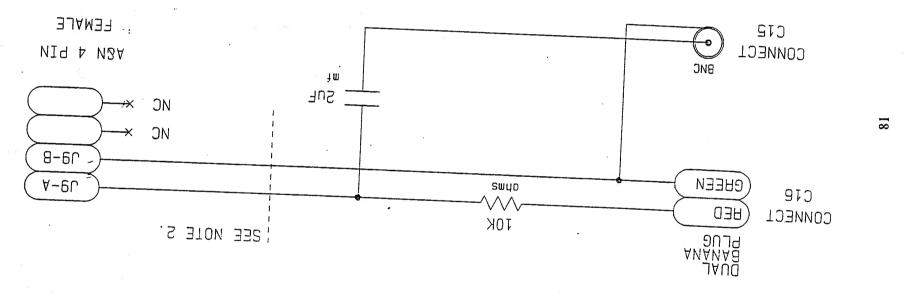


Figure 3. Tachometer Board

G-METER OUTPUT



NOTES: 1. MINI-BOX MOUNTED INSIDE CHASSIS NEXT TO JUNCTION BOX

2. USE 1/4" GROMMET THRU MINI-BOX FOR THESE CONDUCTORS

3. PCAD 6.00 FILE GMETER SCH, DATE 2/24/93

Figure 4. G-meter Output Circuit

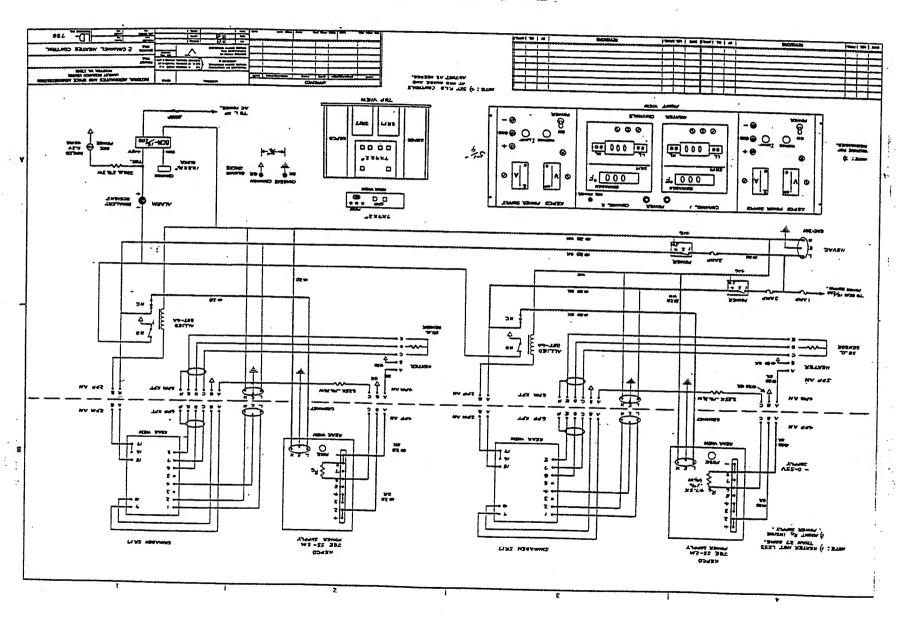


Figure 5. Heater Controller

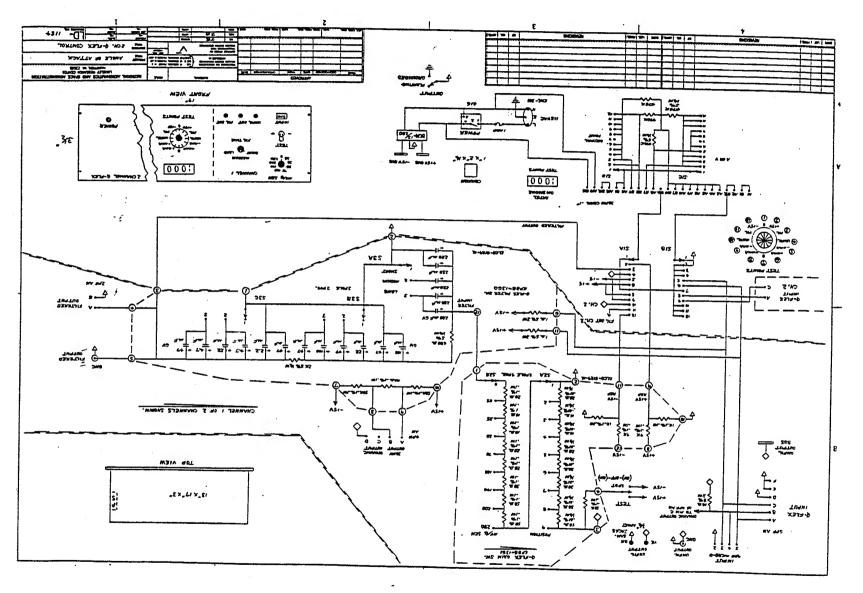


Figure 6. Q-Flex Chassis

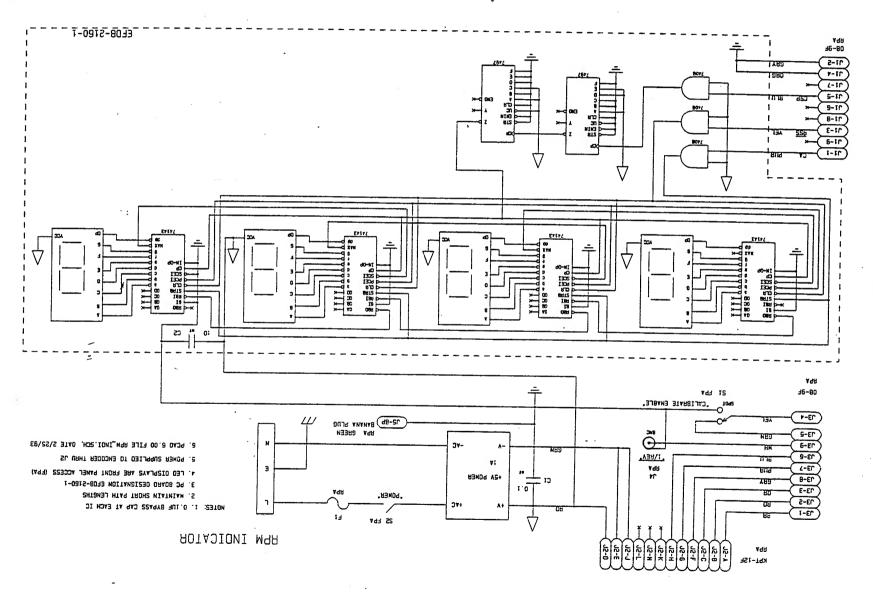


Figure 7. RPM Indicator Chassis

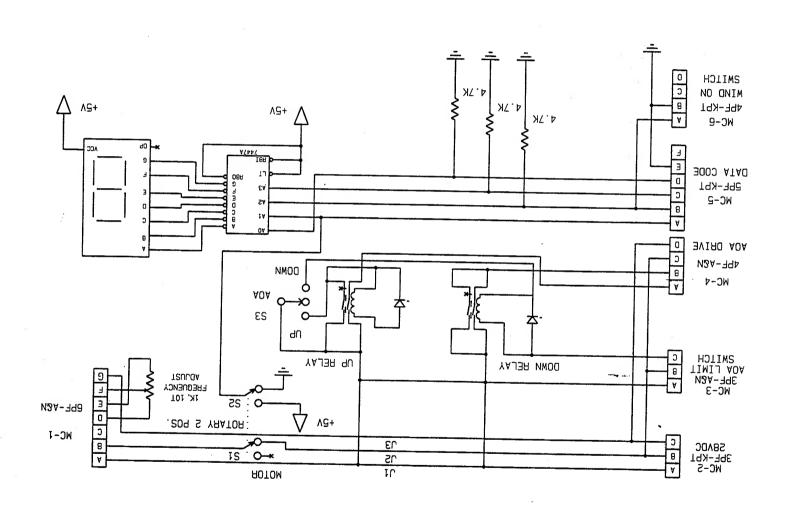


Figure 8. Motor Speed Controller and Data Code Display

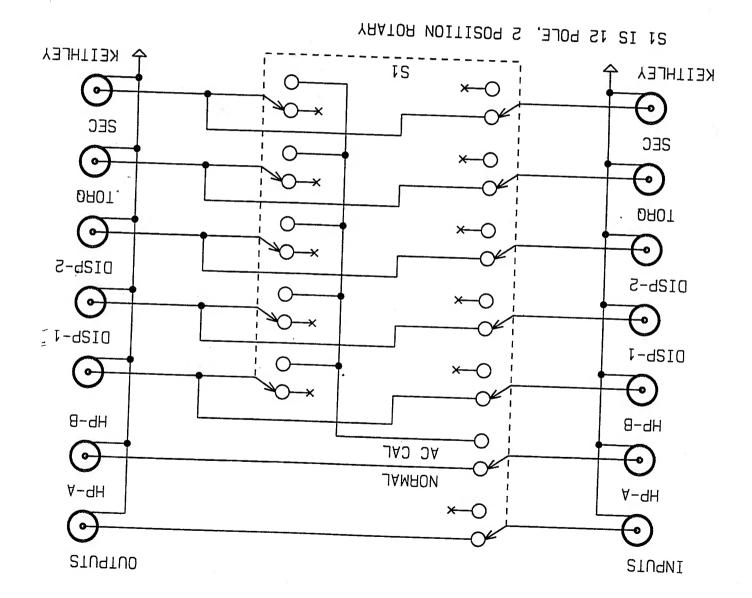


Figure 9. AC CAL Switch Panel

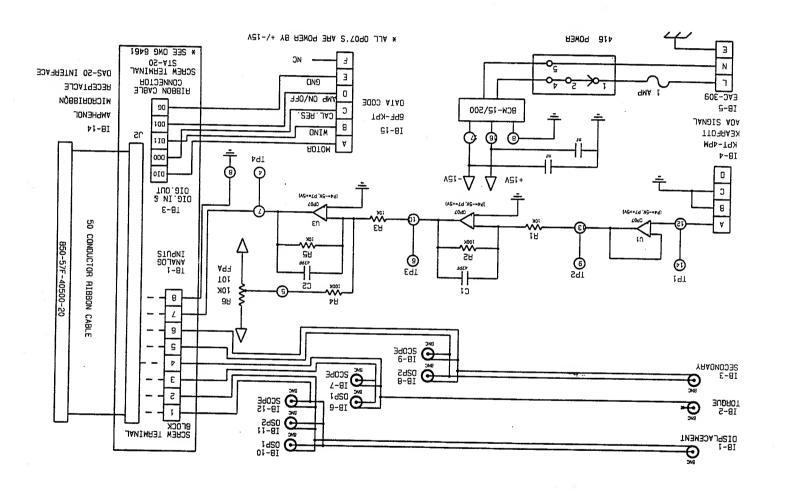


Figure 10. Main Interface Chassis

Public reporting burden for this collection of information is estimated to average 1 hour per response, including the time for reviewing instructions, searching existing data sources, gathering and maintaining the data needed, and completing and reviewing the collection of information. Send comments regarding this burden estimate or any other aspect of this collection of information, including suggestions for reducing this burden, to Washington Headquarters Services, Directorate for Information Operations and Reports, 1215 Jefferson Davis Highway, Sulte 1204, Allington, NA 22202-4302, and to the Office of Management and Budget, Paperwork Reduction Project (0704-0789), Washington, DC 20503. 1. AGENCY USE ONLY (Leave blank) 2. REPORT DATE 3. REPORT TYPE AND DATES COVERED instrumentation rack include a personal computer, digital signal processor microcomputers, computer-controlled signal conditioners, function generator, digital multimeter, and an optional Fast Fourier Transfor Analyzer. specific NASA Langley wind tunnels. The instrumentation system performs either a synchronous demodulation This paper is a hardware description manual for the Dynamic Stability Instrumentation System that is used in Form Approved OMB No. 0704-0188 or a Fast Fourier Transform on dynamic balance strain gage signals, and ultimately computes aerodynamic coefficients. The DSIS consists of a double rack of instruments, a remote motor-generator set, two specical 20. LIMITATION OF ABSTRACT 8. PERFORMING ORGANIZATION REPORT NUMBER 10. SPONSORING / MONITORING AGENCY REPORT NUMBER 15. NUMBER OF PAGES stings each with motor driven shafts, and specially designed balances. The major components in the A05 8 NASA TM 109160 12b. DISTRIBUTION CODE 5. FUNDING NUMBERS 6. PRICE CODE 505-59-54-02 Technical Memorandum 19. SECURITY CLASSIFICATION OF ABSTRACT Unclassified T. L. Jordan, T. S. Daniels, D. A. Hare, R. P. Boyden, and D. A. Dress REPORT DOCUMENTATION PAGE November 1994 Dynamic Stability Instrumentation System 9. SPONSORING / MONITORING AGENCY NAME(S) AND ADDRESS(ES) Aerodynamic stability coefficients PC based data acquisition and reduction 18. SECURITY CLASSIFICATION OF THIS PAGE Dynamic Stability Instrumentation System (DSIS) . PERFORMING ORGANIZATION NAME(S) AND ADDRESS(ES) National Aeronautics and Space Administration Washington, DC 20546-0001 Unclassified 2a. DISTRIBUTION / AVAILABILITY STATEMENT Digital signal processing SECURITY CLASSIFICATION 18. SECURION OF REPORT Volume I: Hardware Description NASA Langley Research Center Hampton, VA 23681-0001 13. ABSTRACT (Maximum 200 words) Unclassified-Unlimited 11. SUPPLEMENTARY NOTES Subject Category 61 Unclassified 4. TITLE AND SUBTITLE 4. SUBJECT TERMS 6. AUTHOR(S)

NSN 7540-01-280-5500